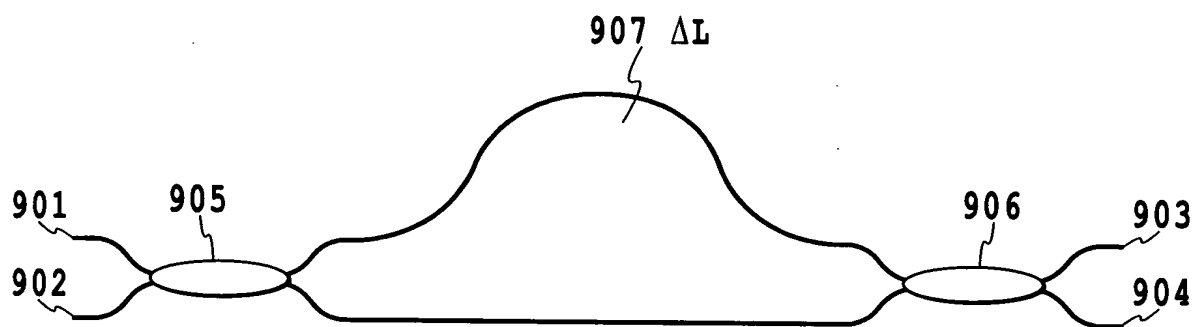
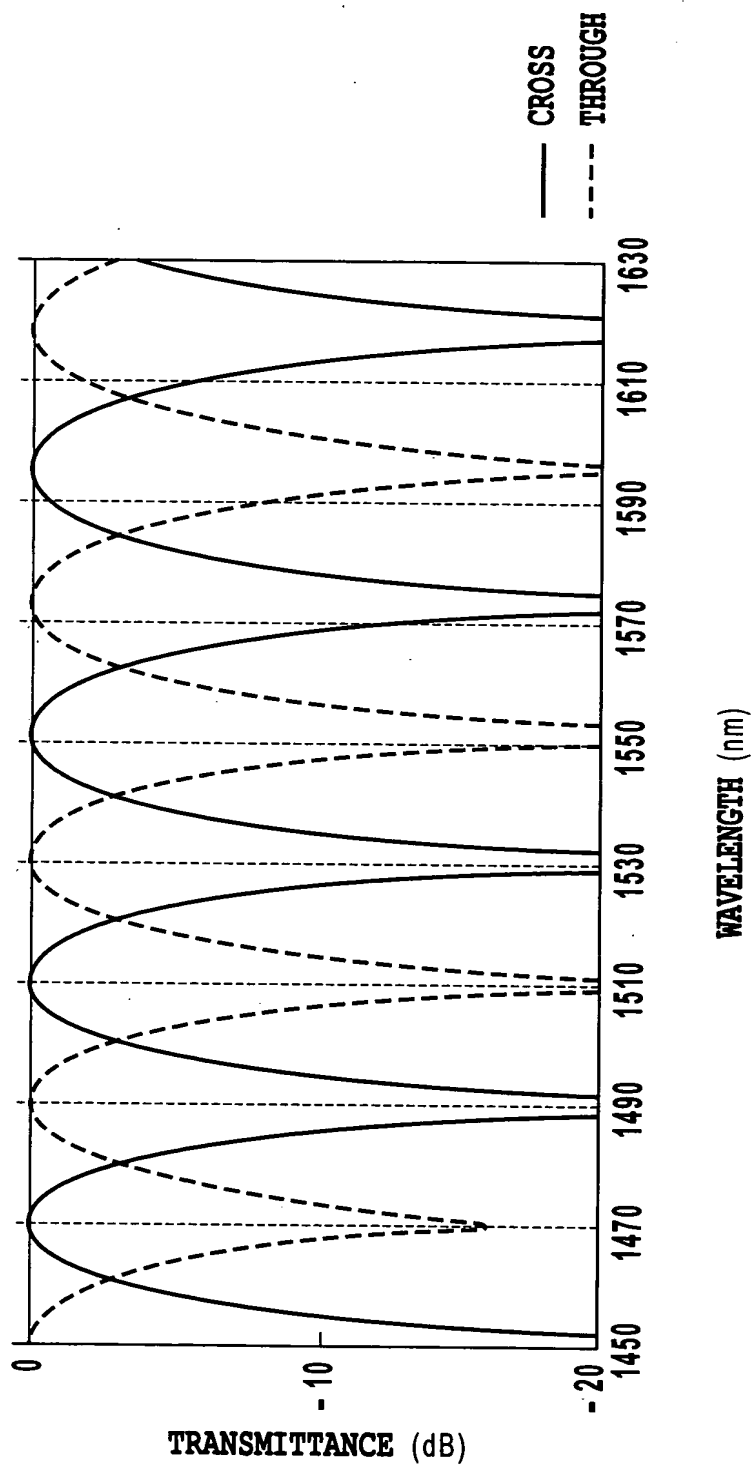


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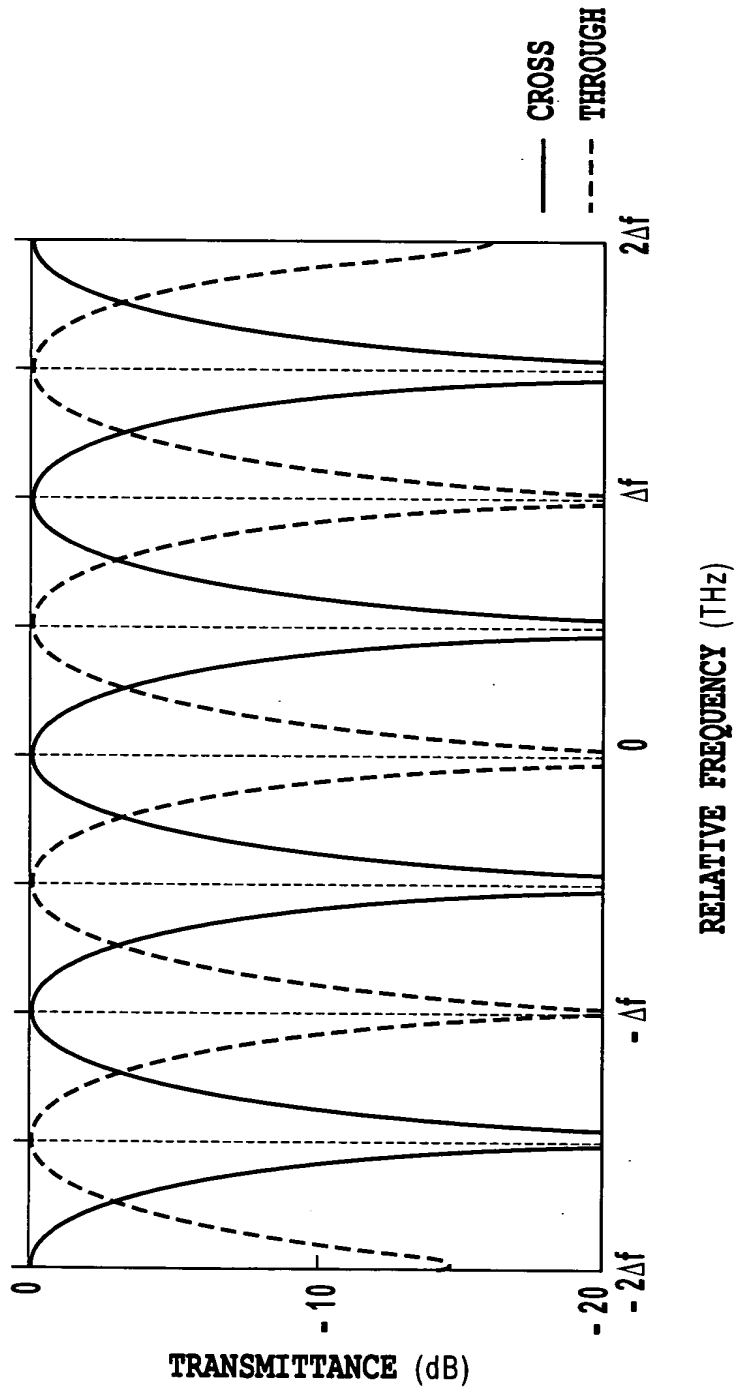
RELATED ART
FIG.1

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RELATED ART

FIG.2



RELATED ART

FIG. 3

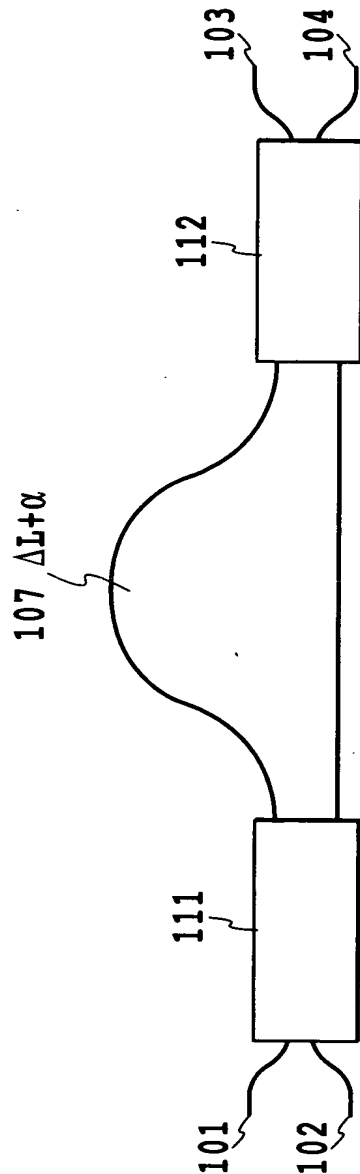


FIG.4

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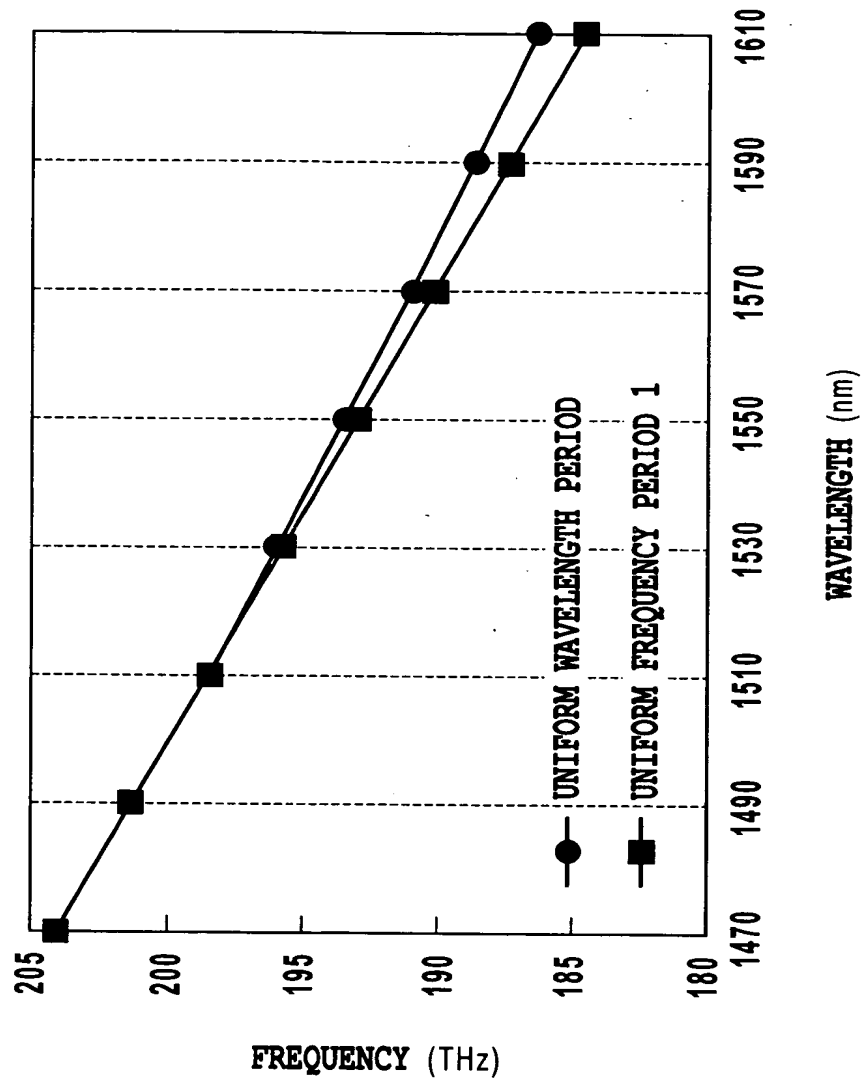


FIG.5

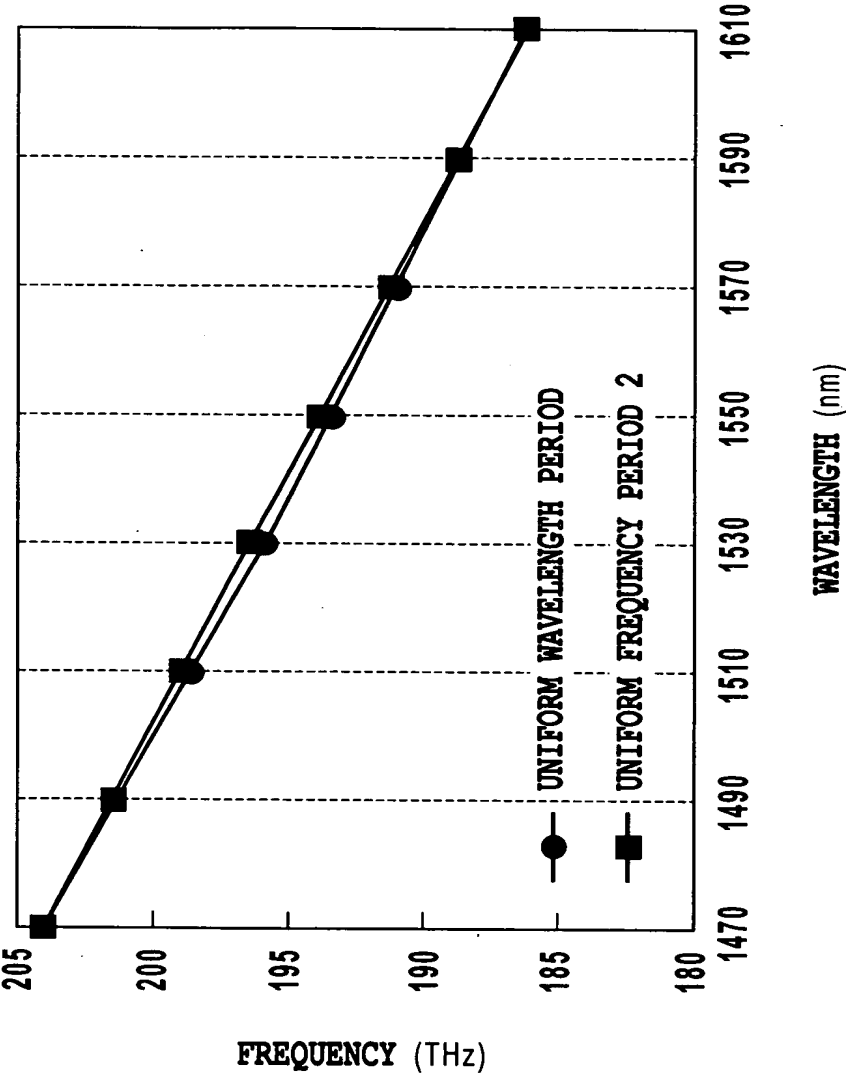


FIG.6

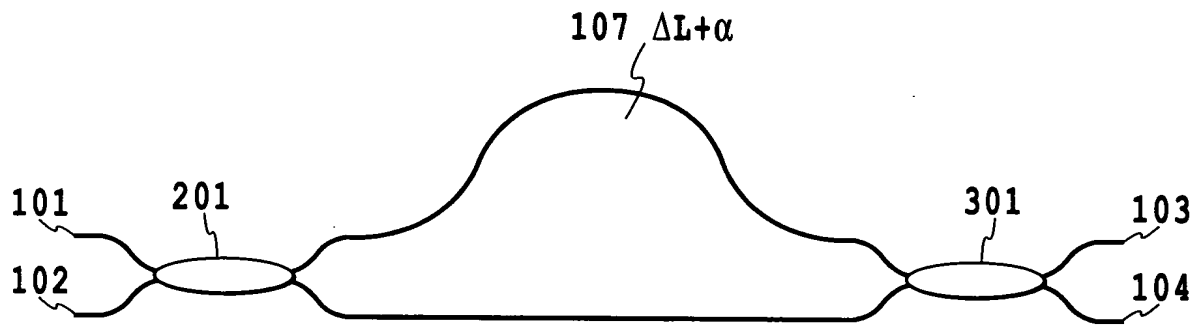


FIG.7

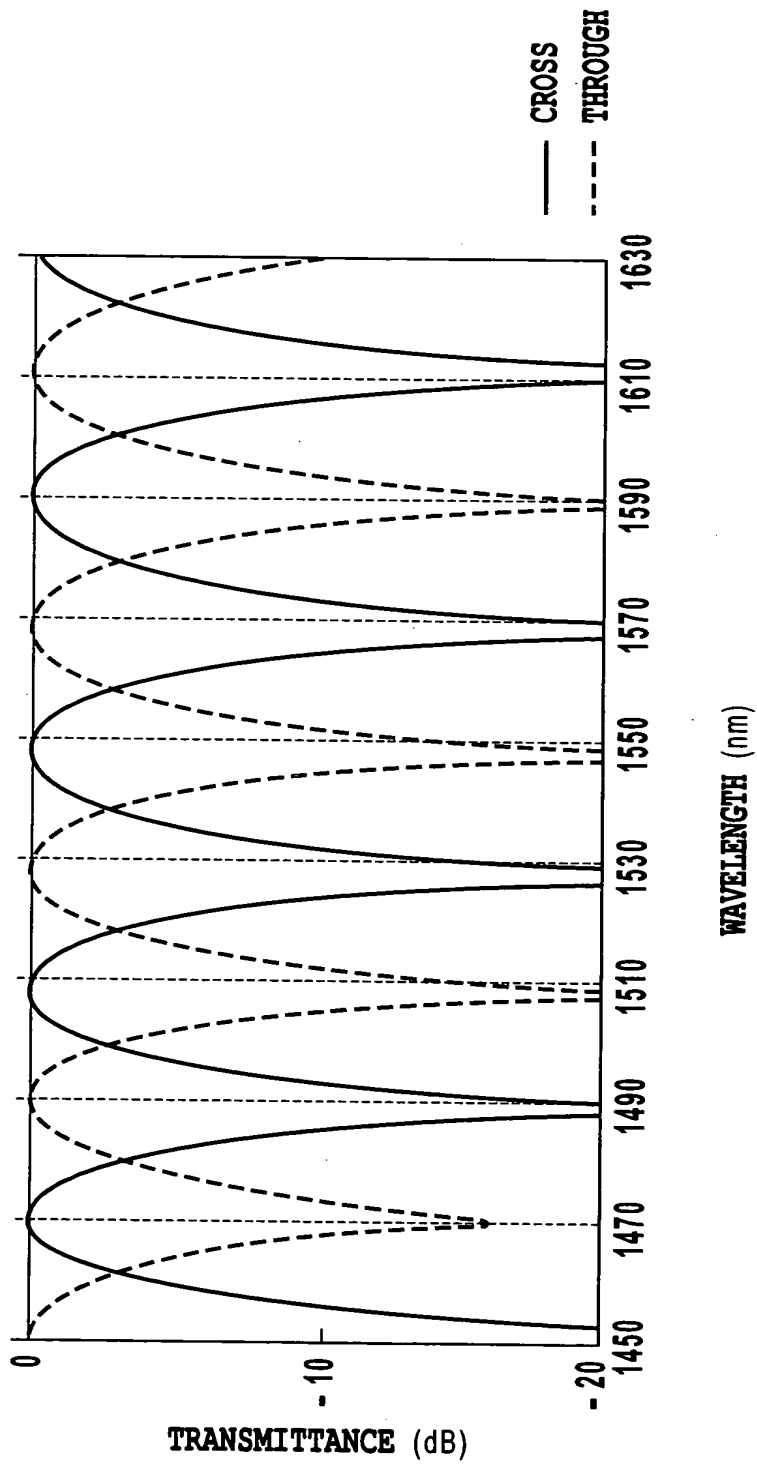


FIG.8

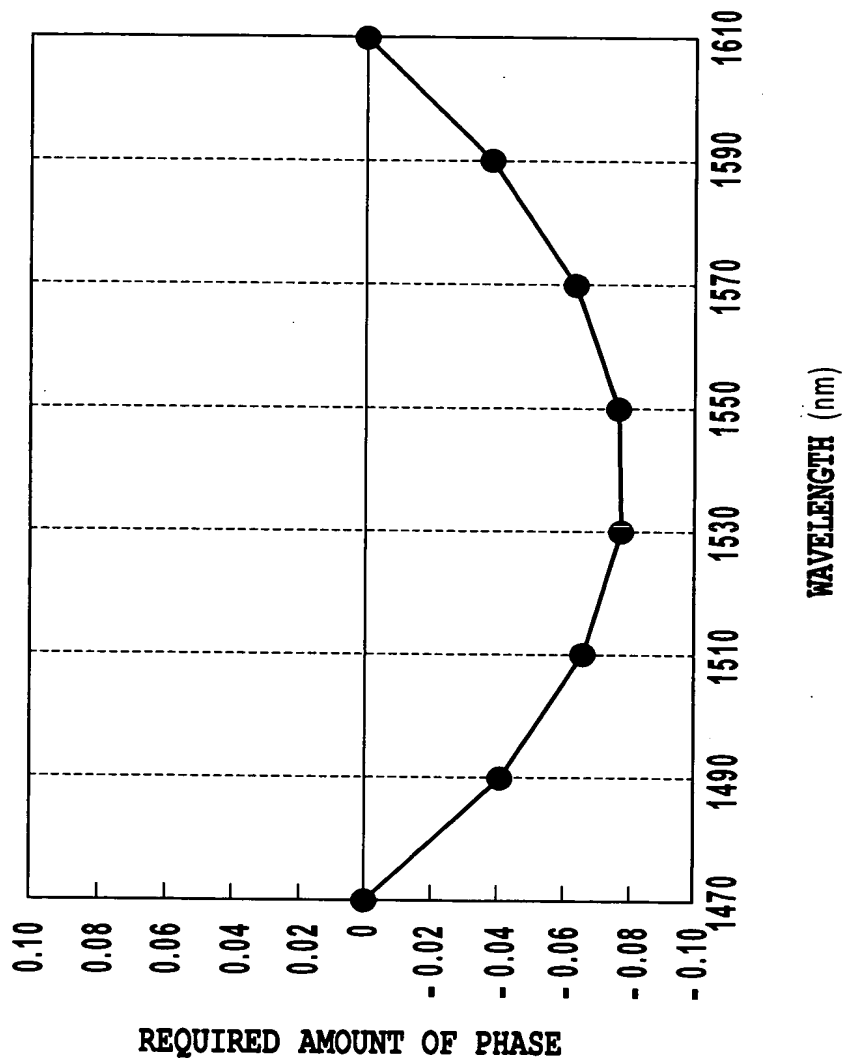


FIG.9

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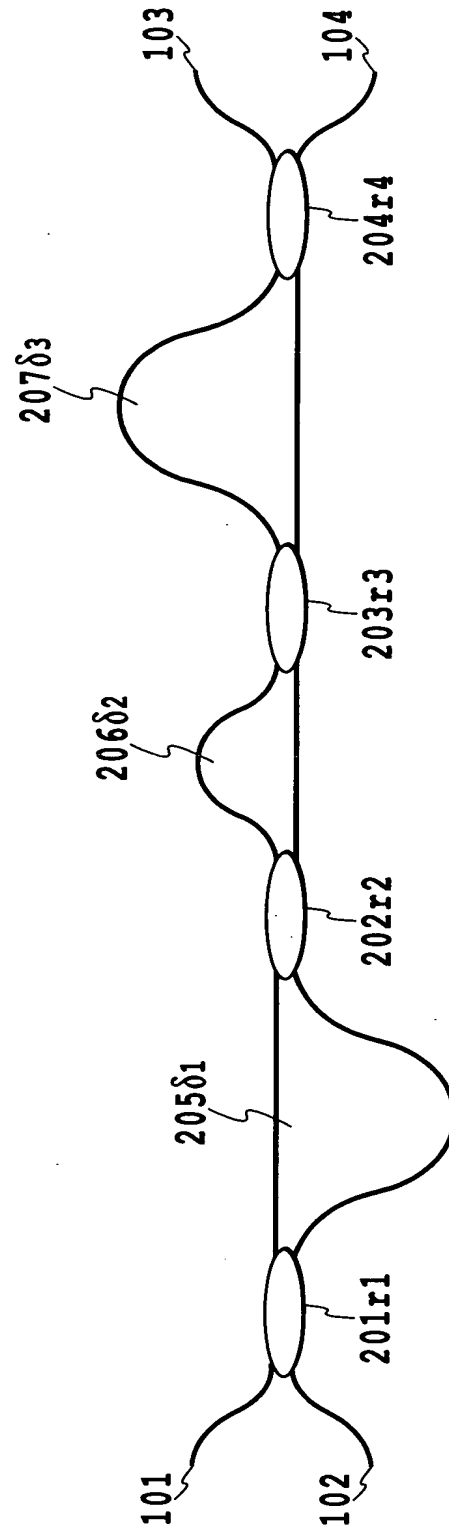


FIG.10

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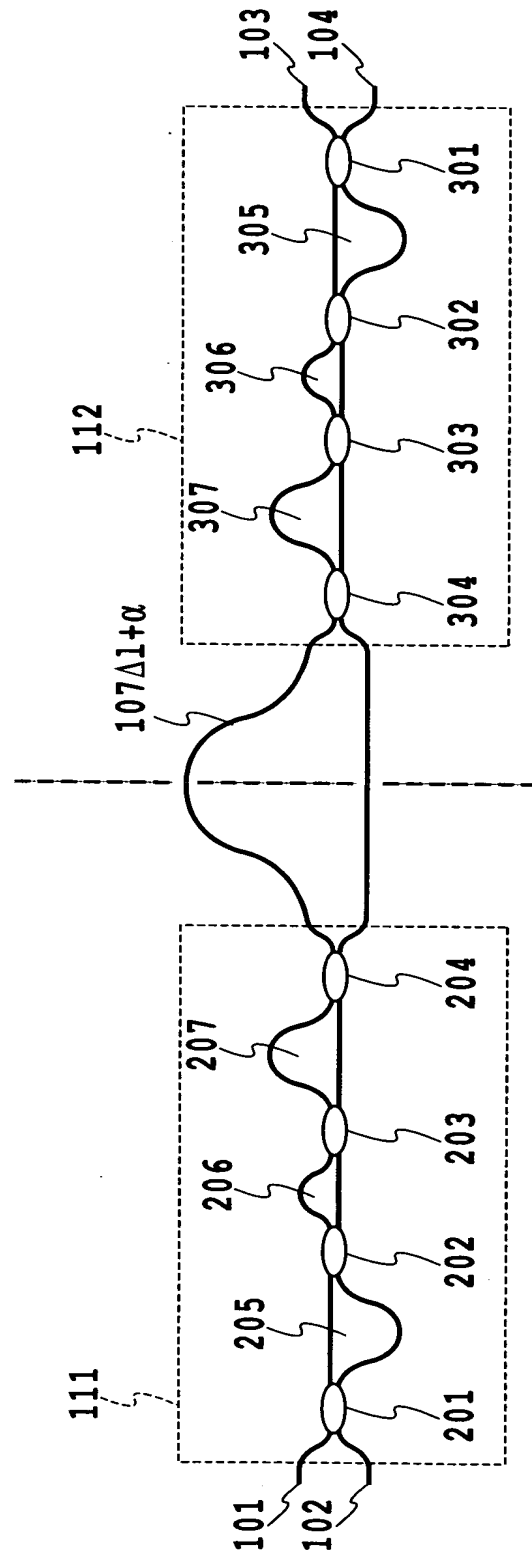


FIG.11

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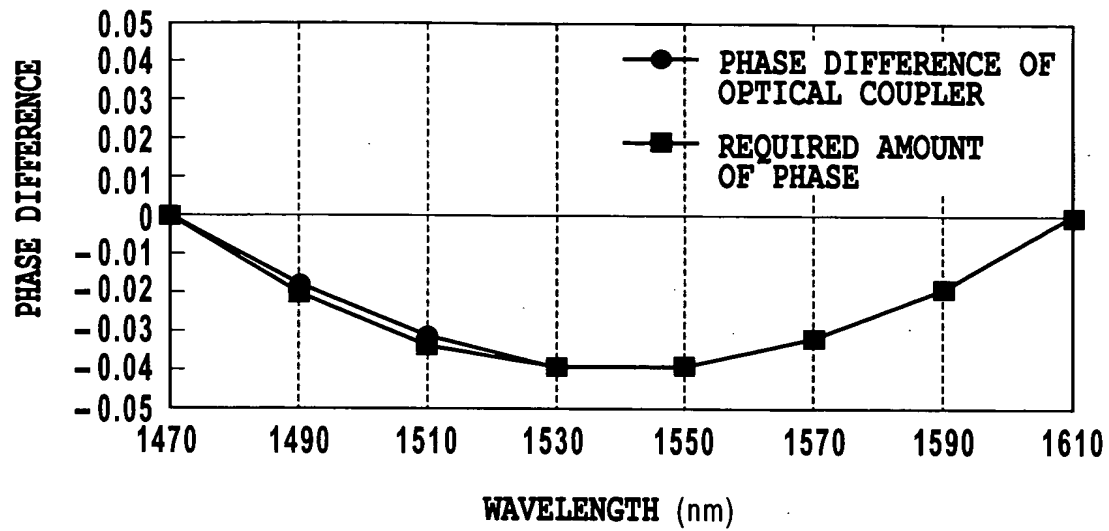


FIG.12A

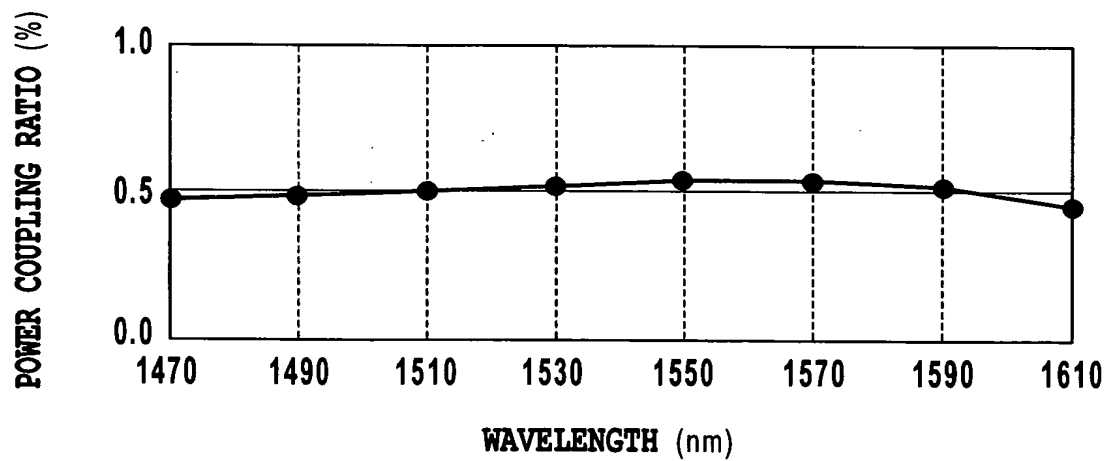


FIG.12B

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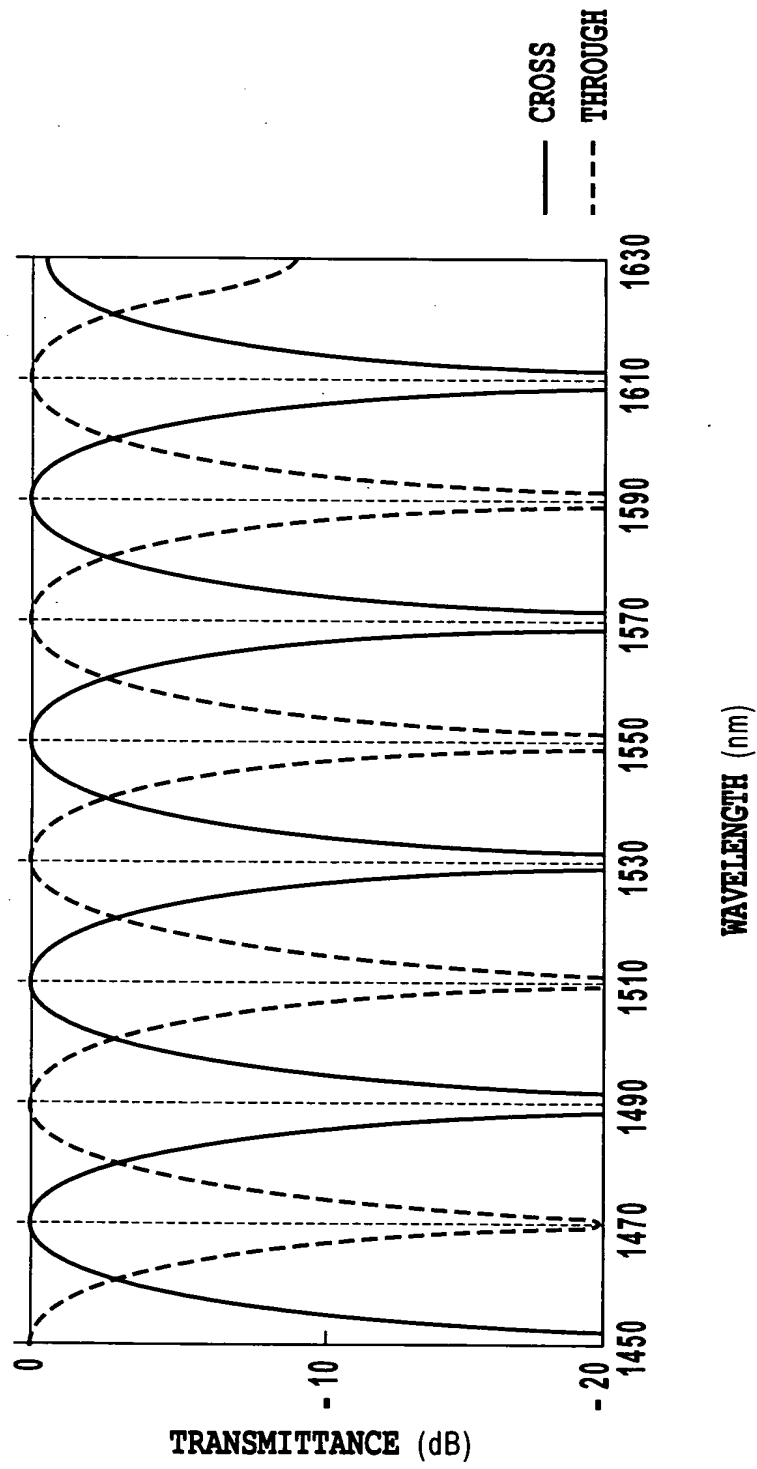


FIG.13

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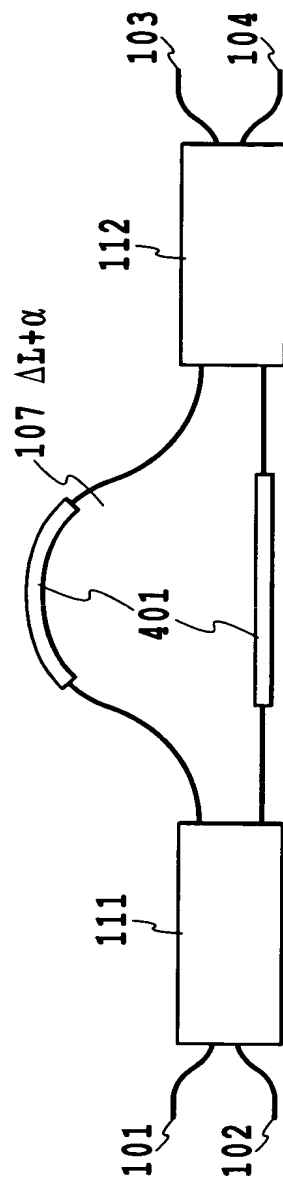


FIG.14

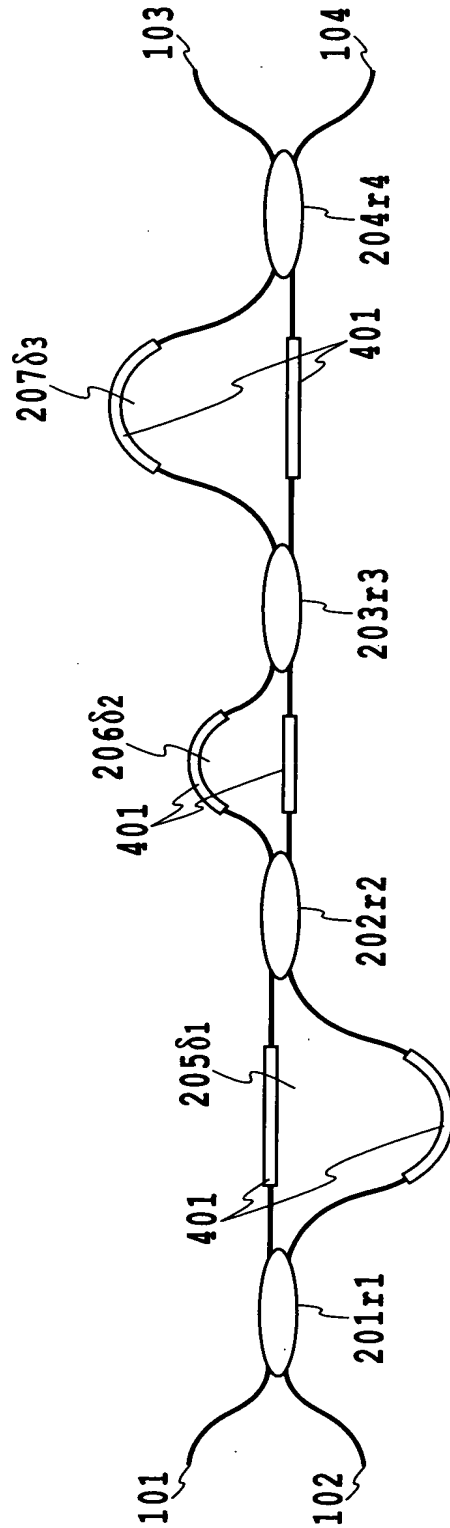


FIG.15

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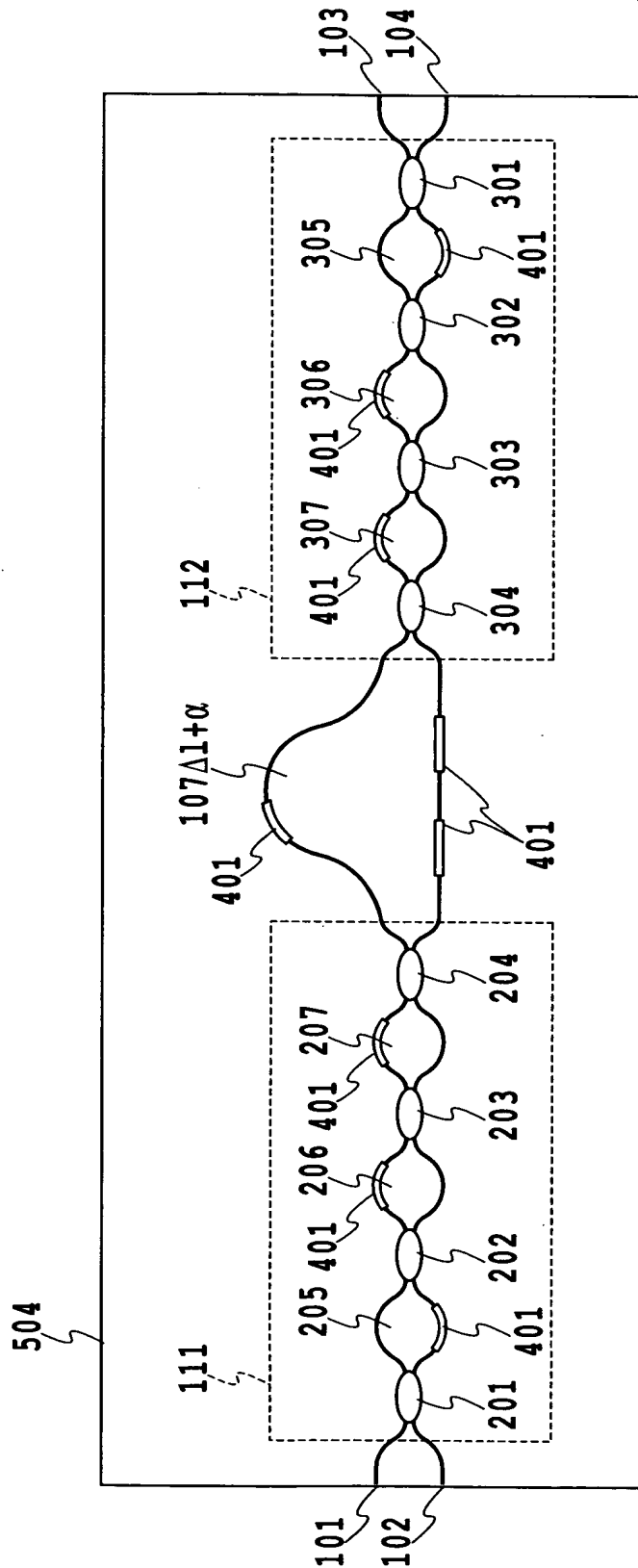


FIG.16

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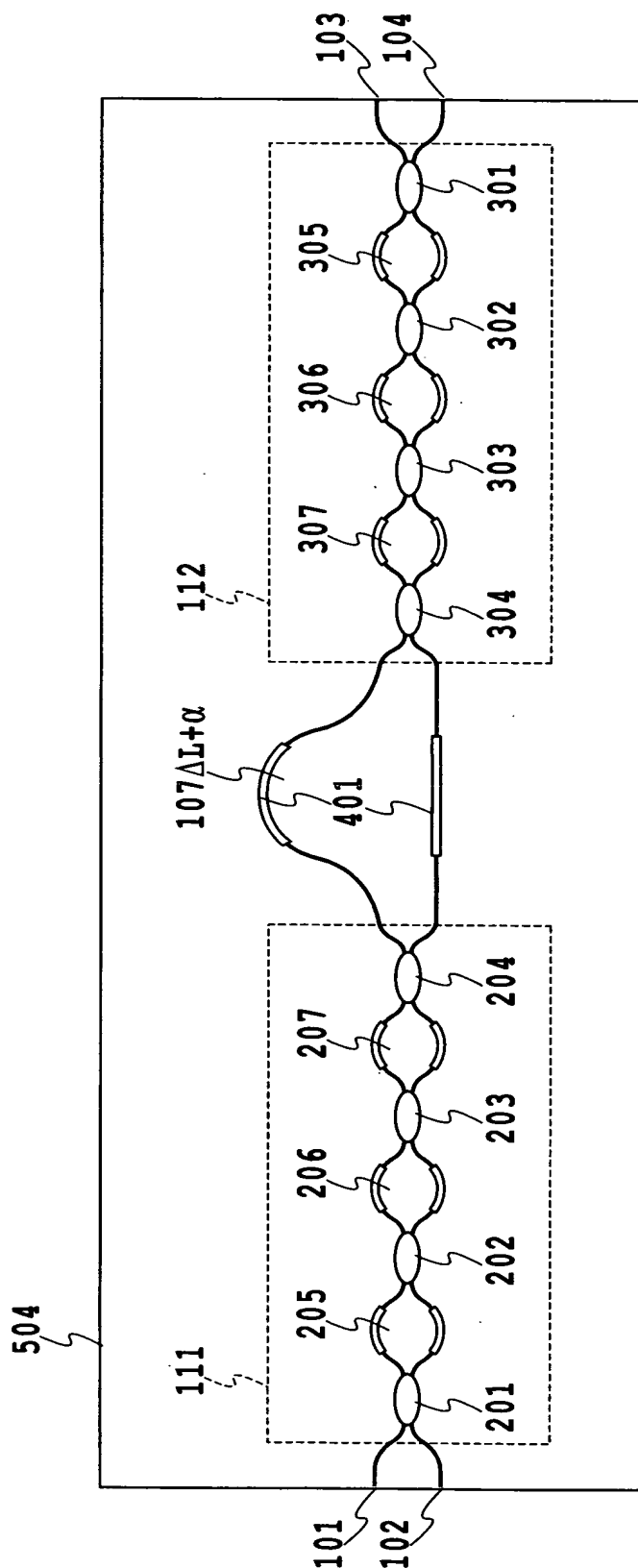


FIG.17

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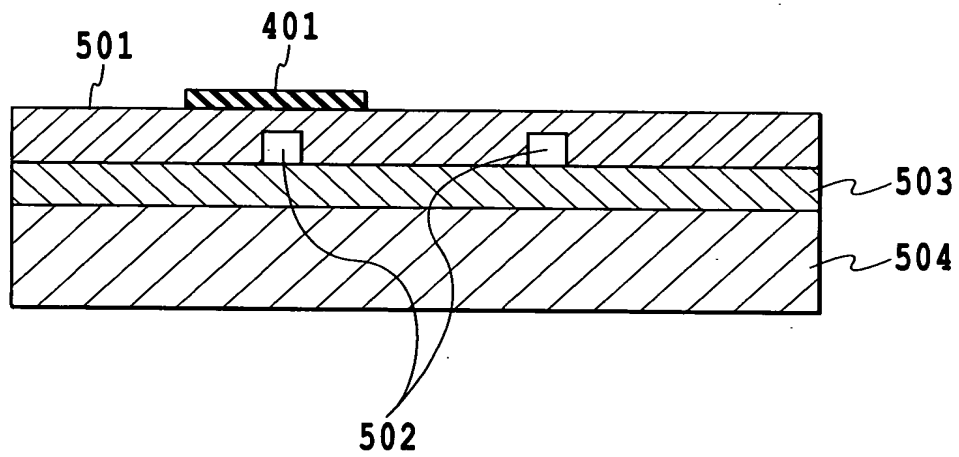
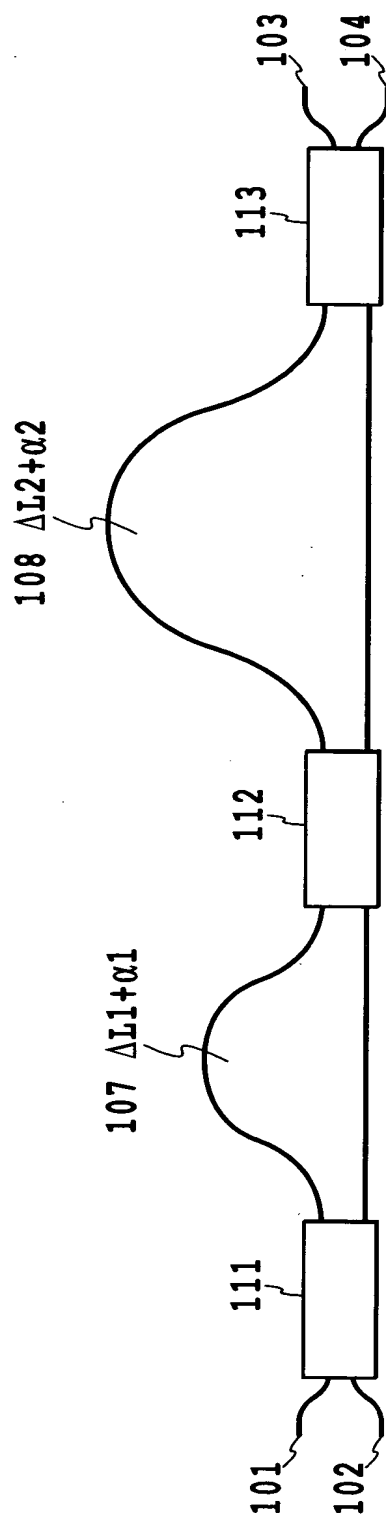


FIG.18



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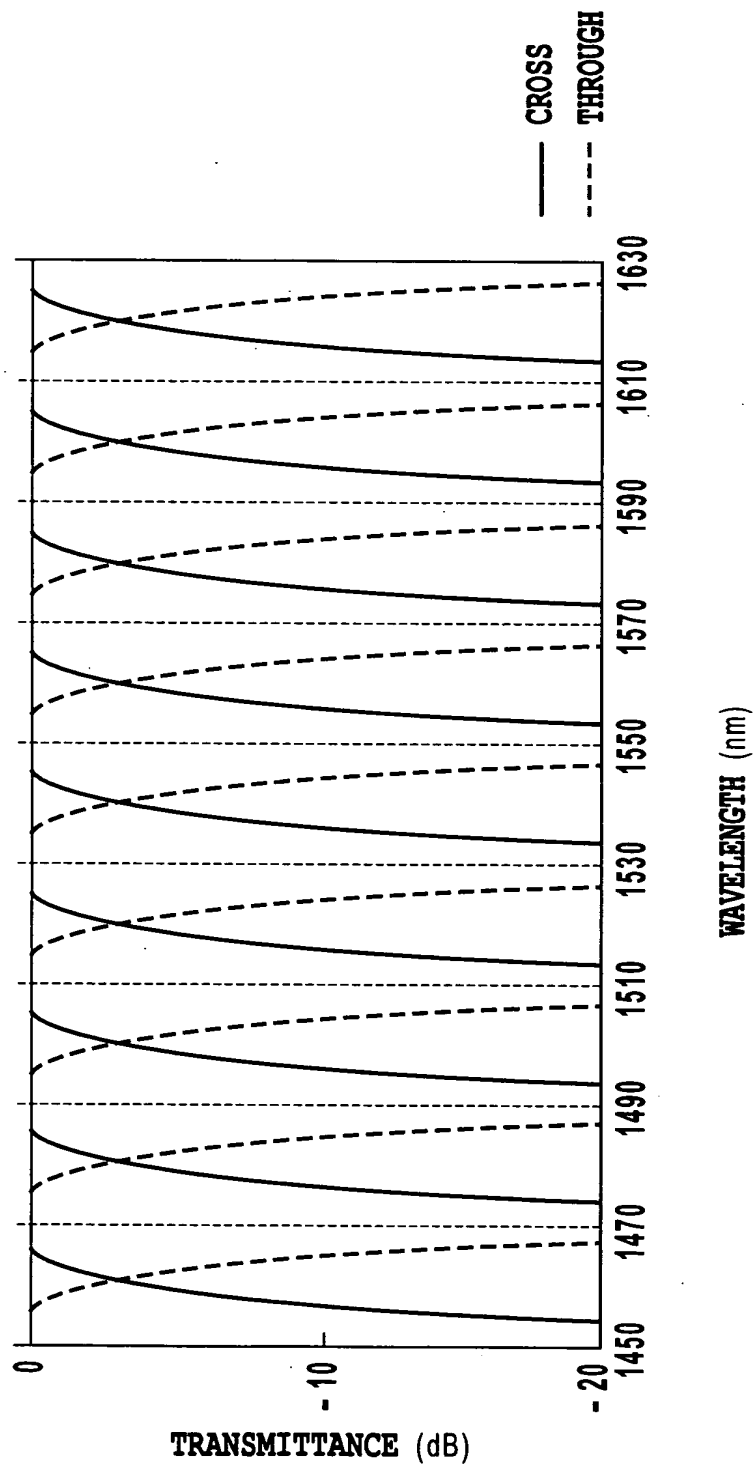


FIG.20

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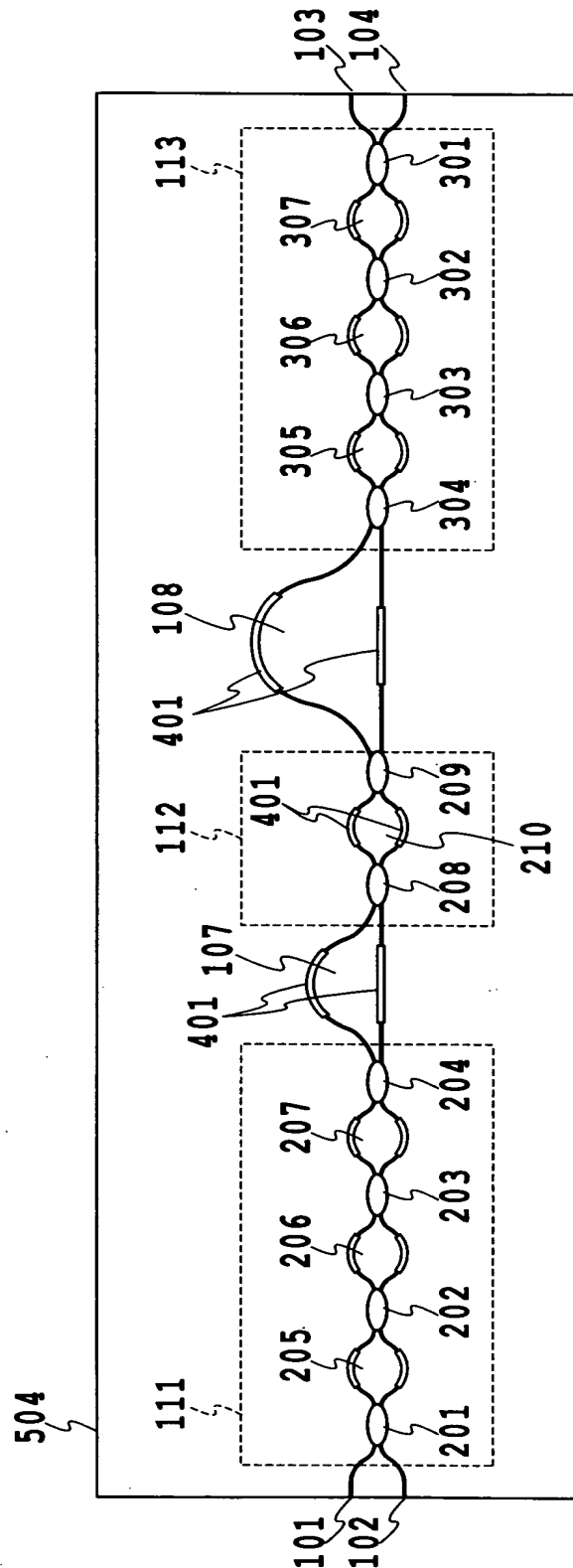


FIG.21

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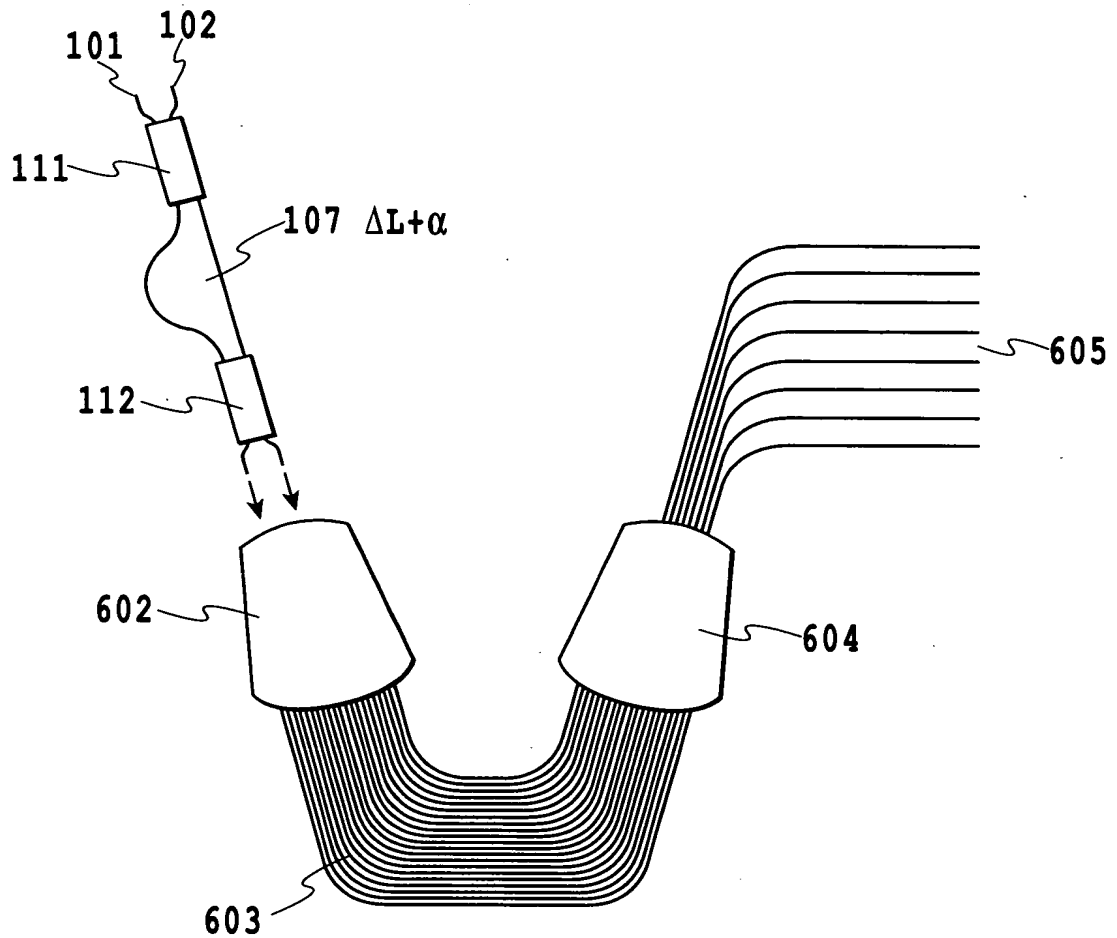


FIG.22

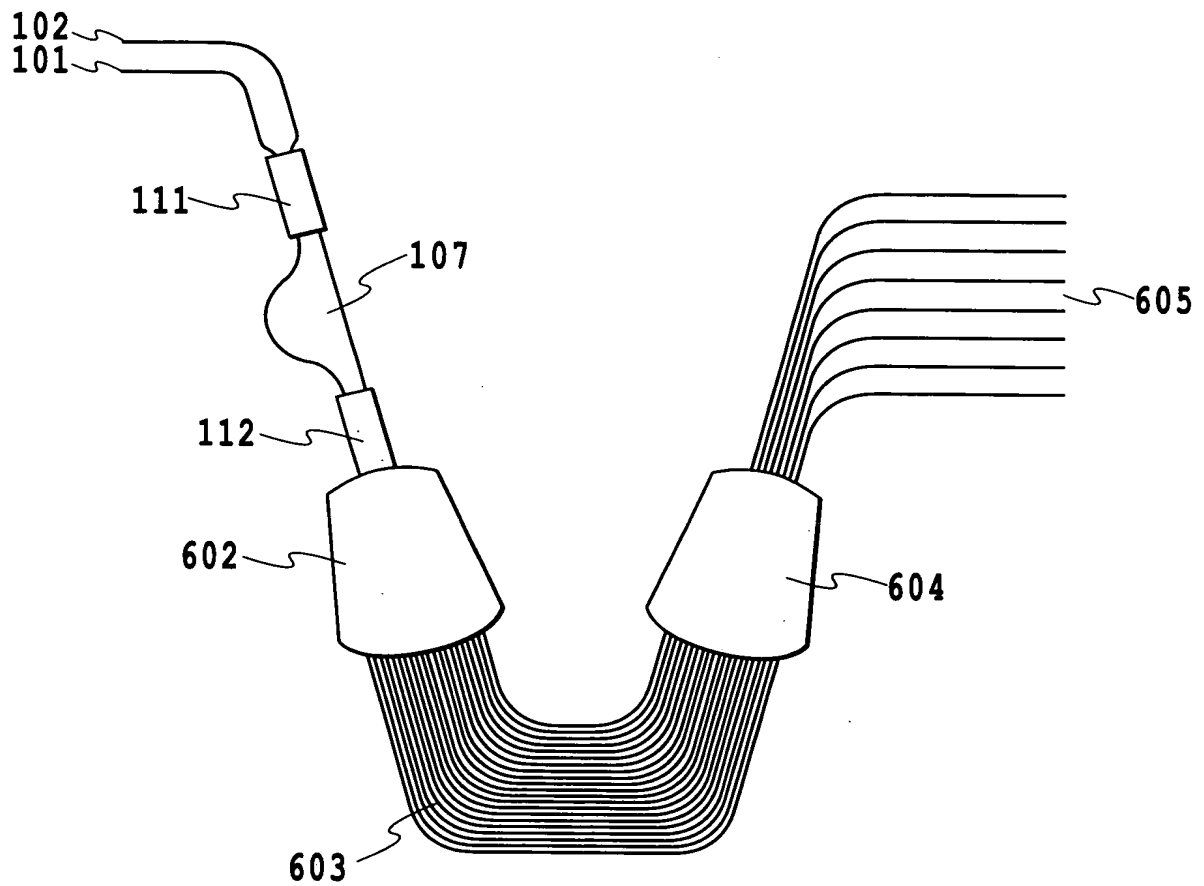


FIG.23

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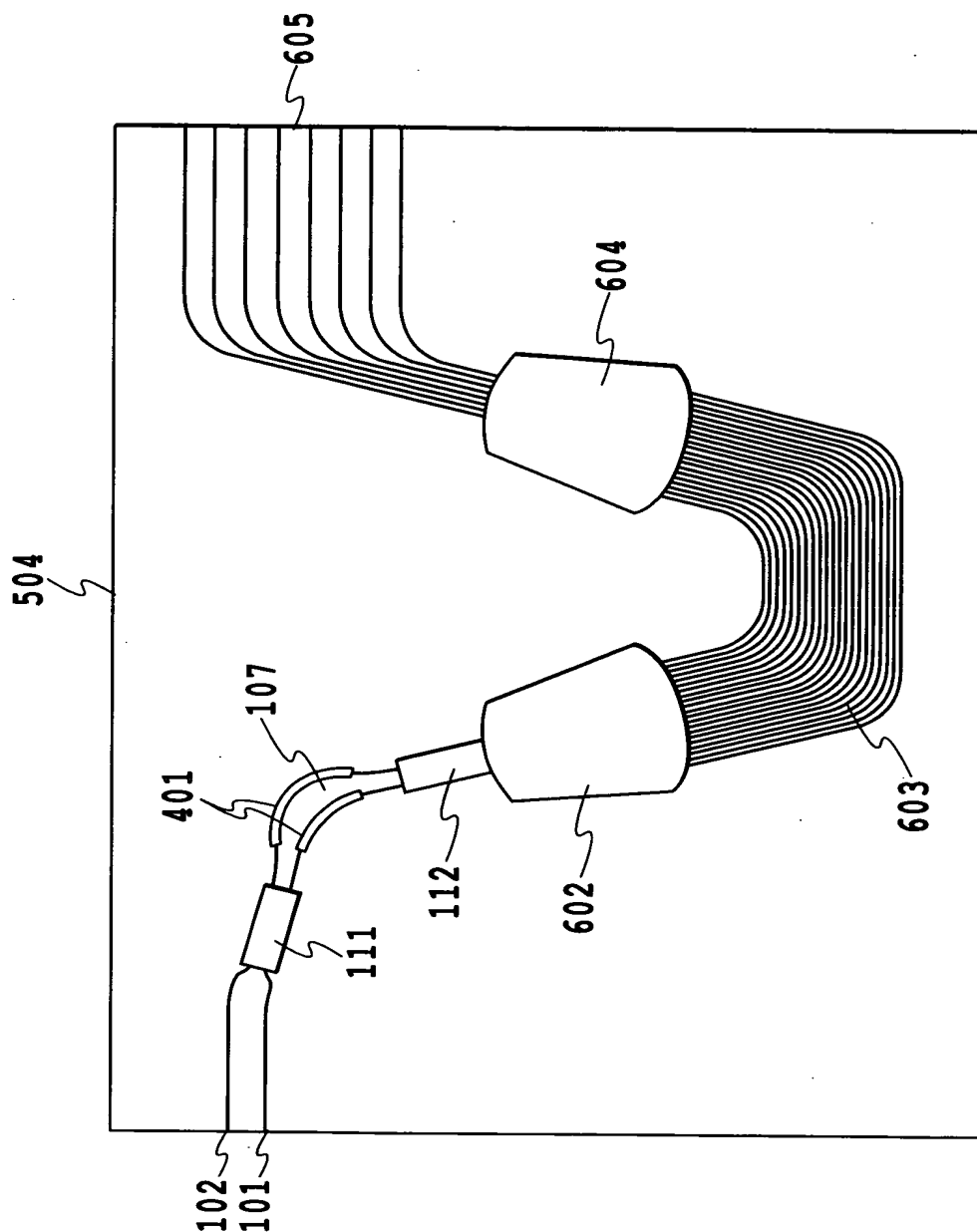


FIG.24

Title: OPTICAL MULTI/DEMULTIPLEXING CIRCUIT
EQUIPPED WITH PHASE GENERATING DEVICE

Inventors: Takayuki Mizuno et al.

Docket No.: 14321.60

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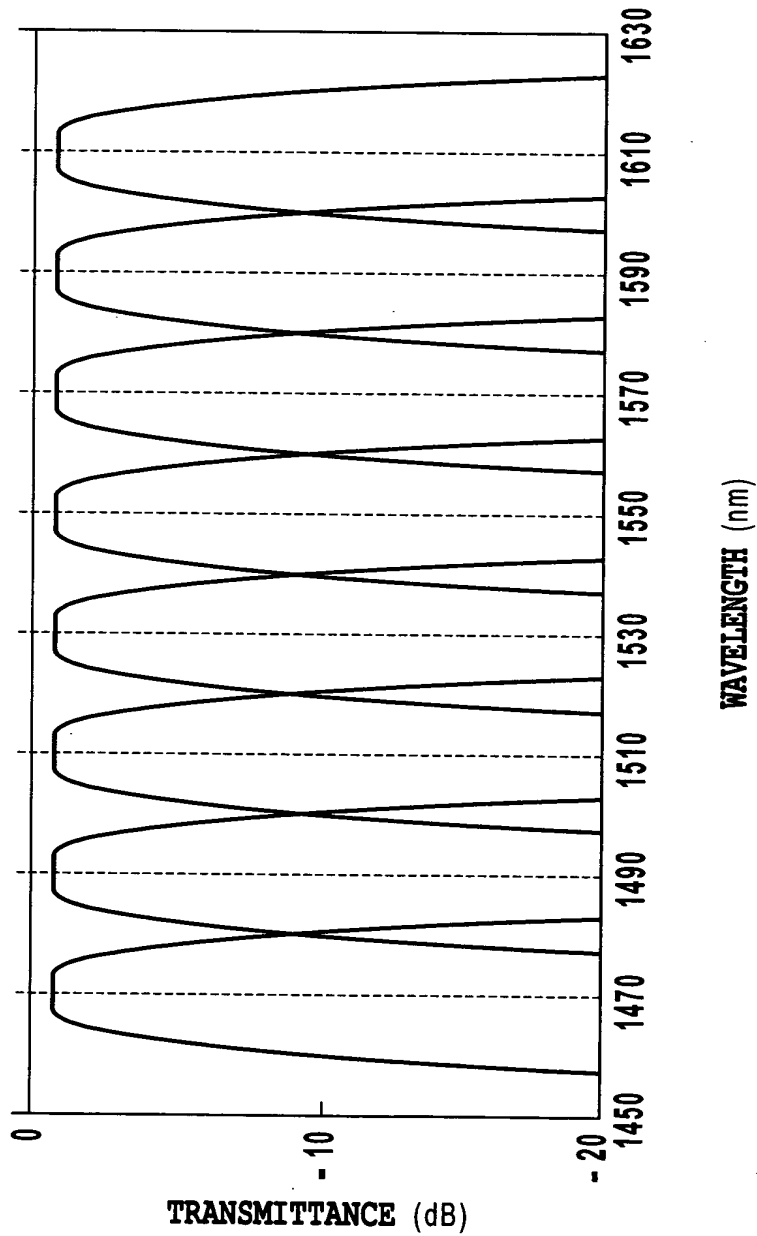


FIG.25

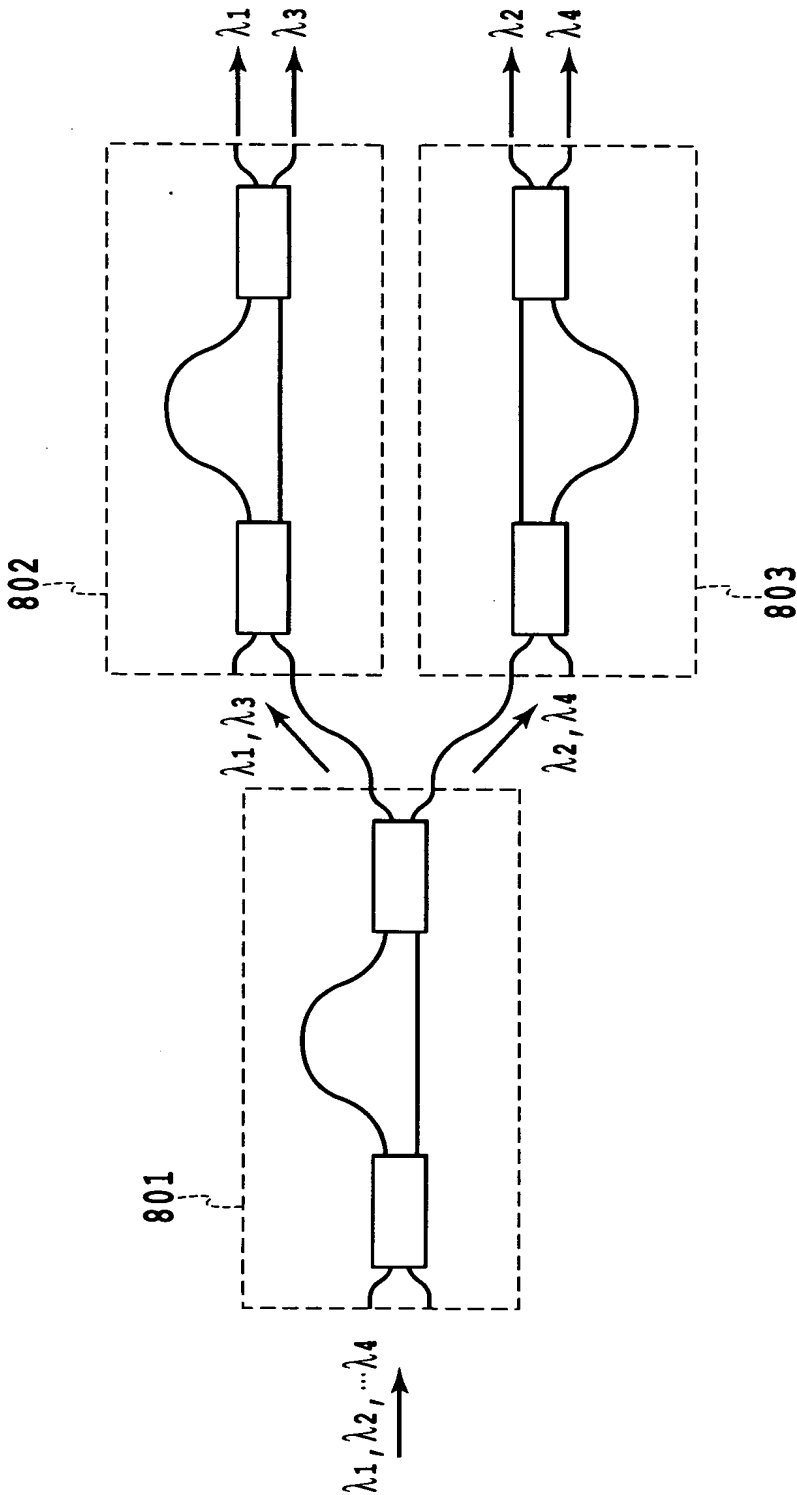


FIG. 26

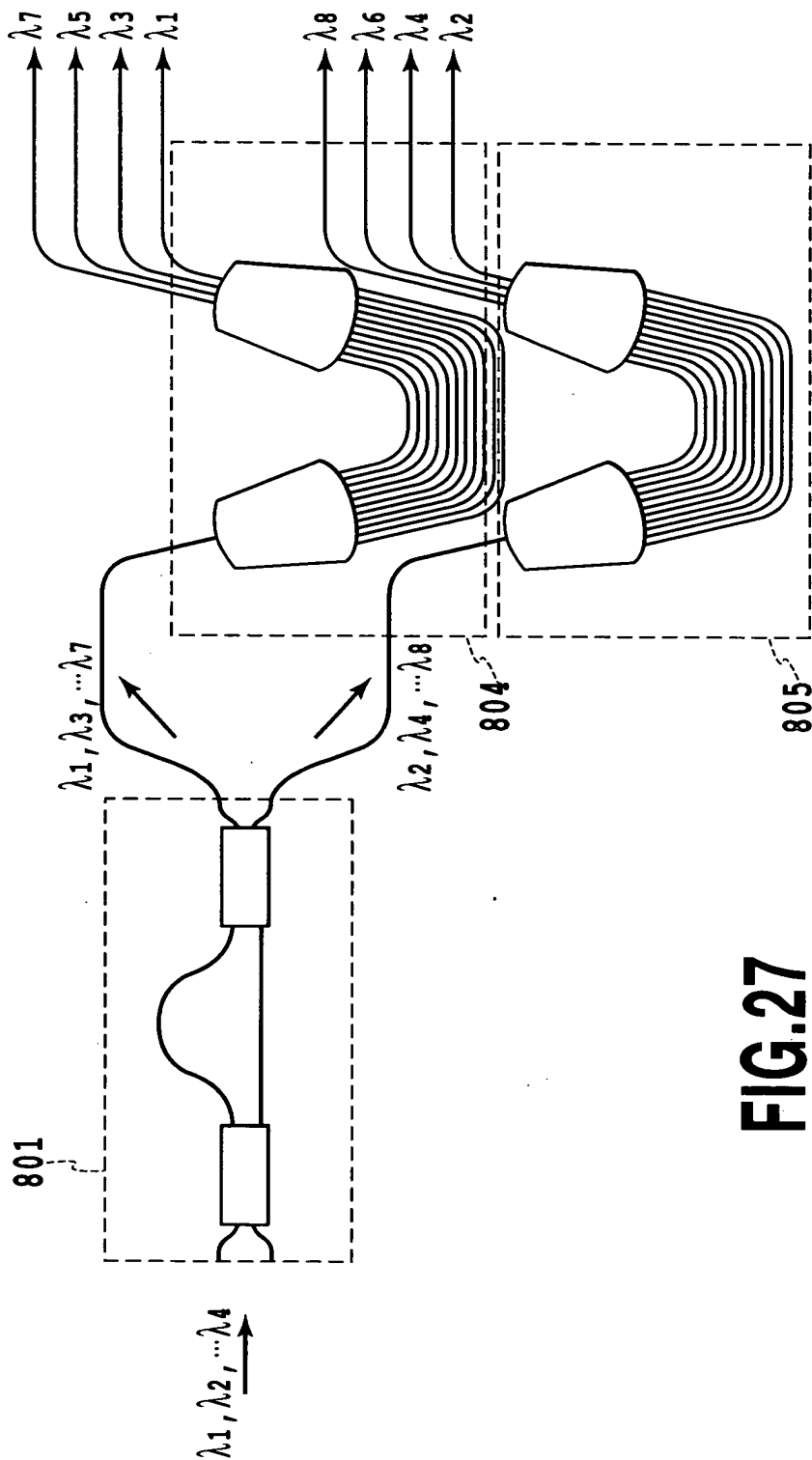


FIG.27

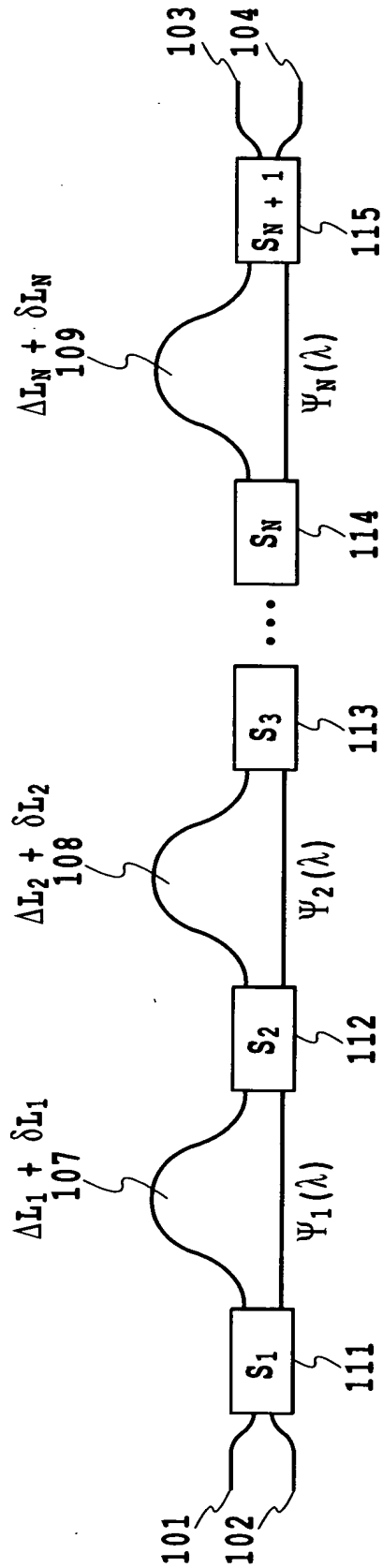


FIG. 28

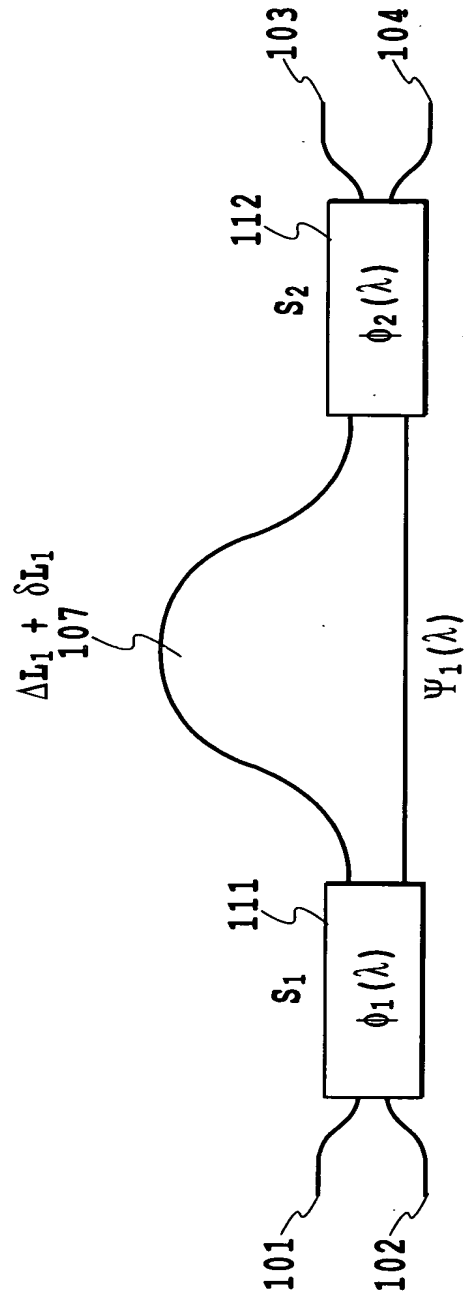


FIG.29

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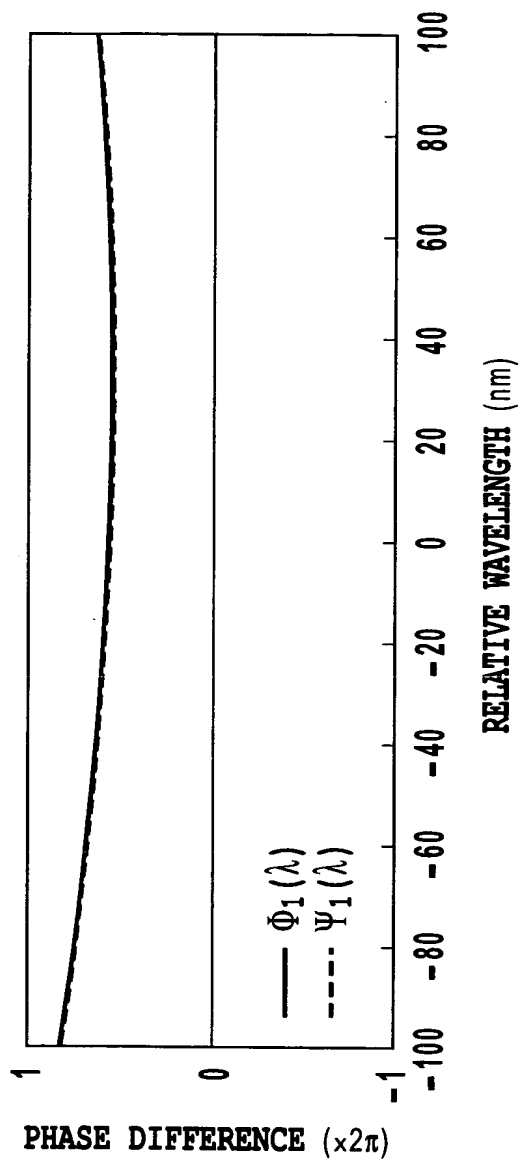


FIG.30A

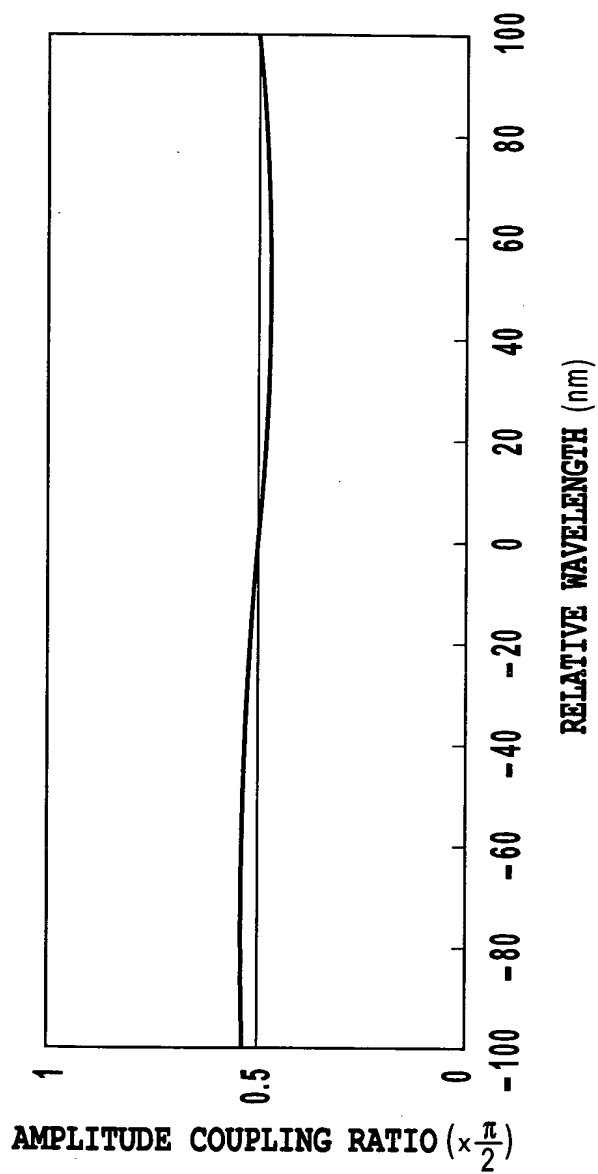


FIG.30B

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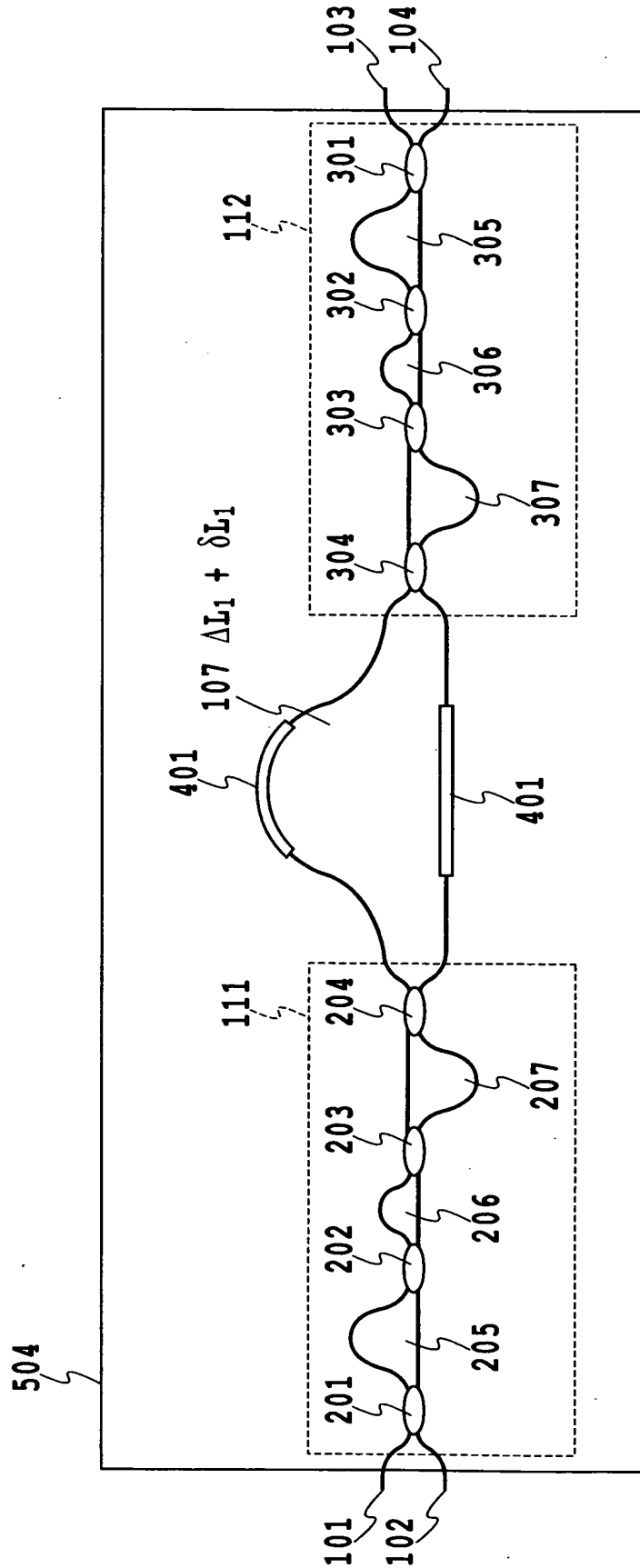


FIG.31

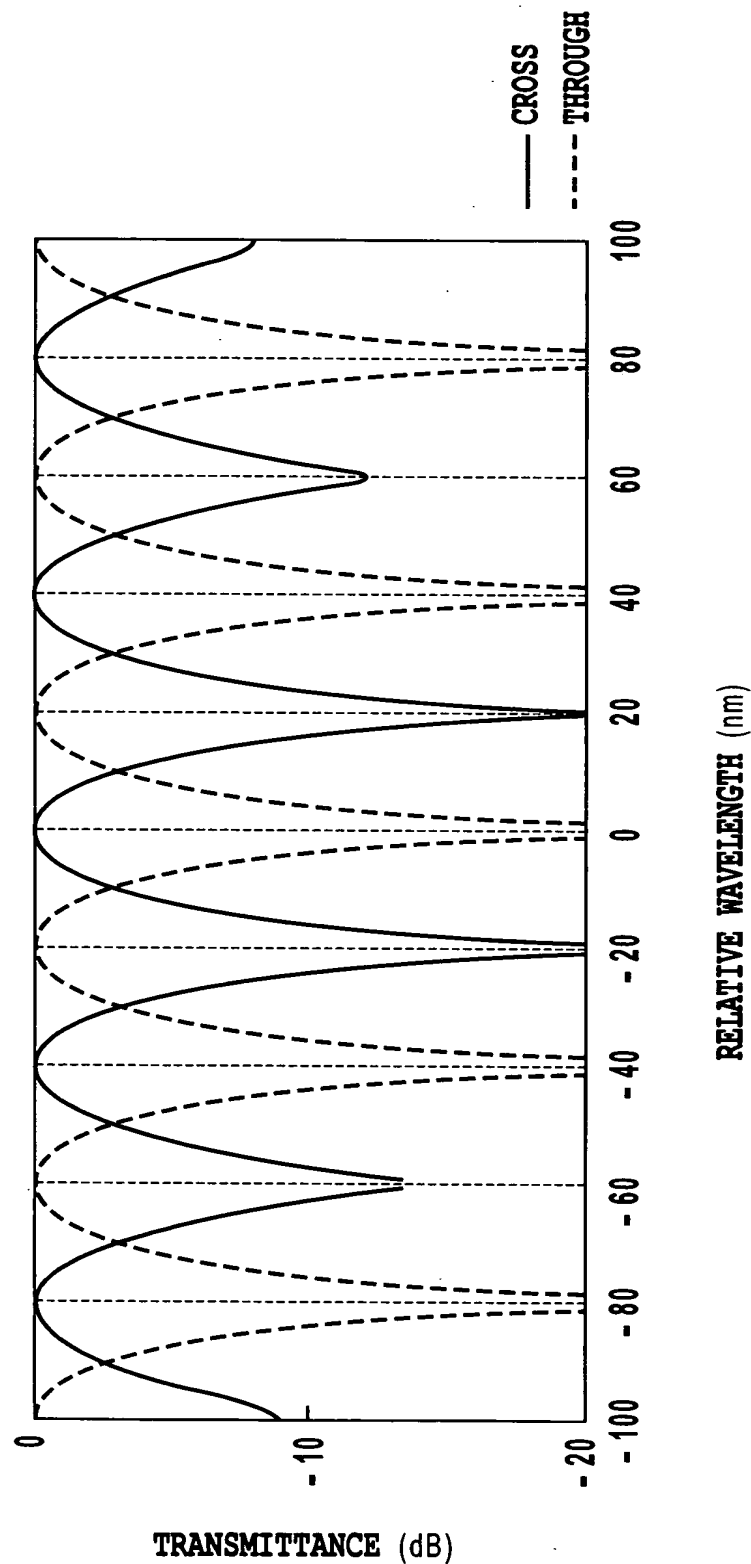


FIG.32

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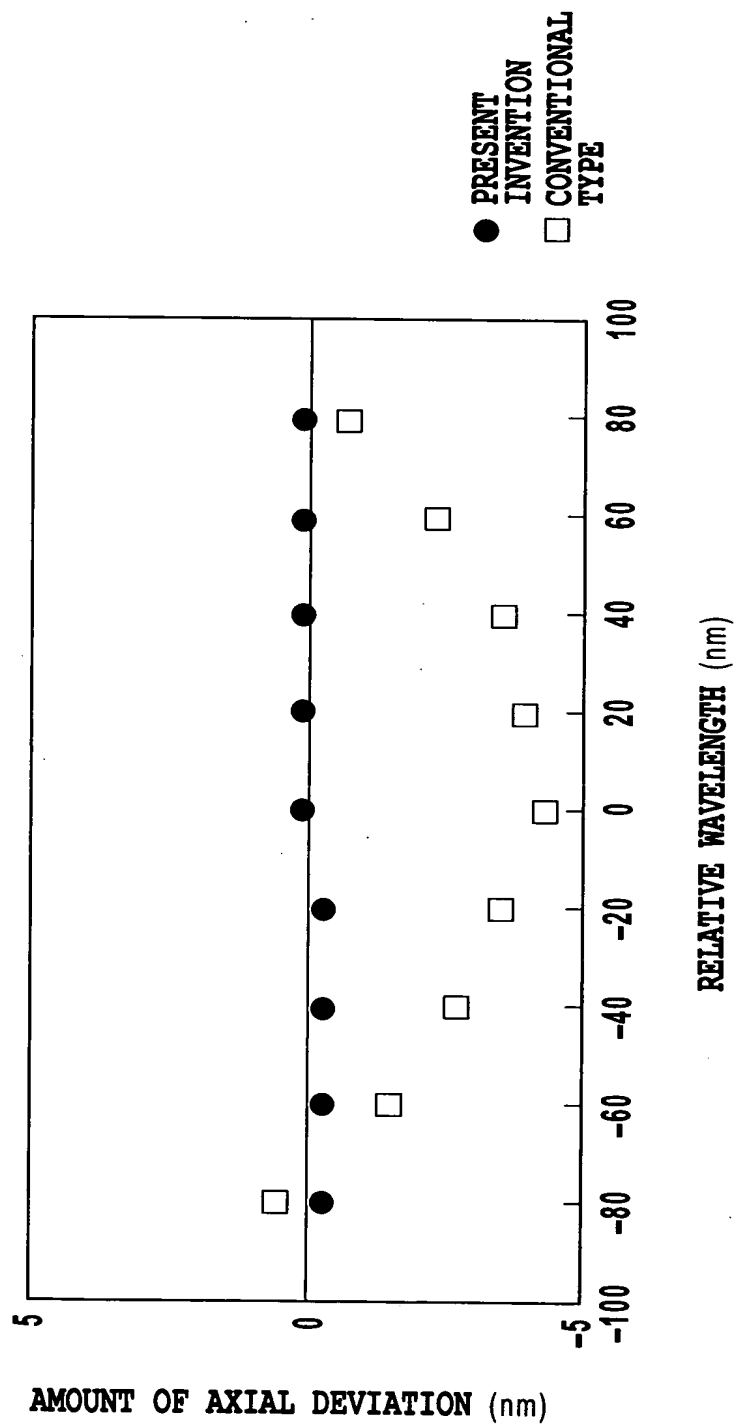


FIG.33

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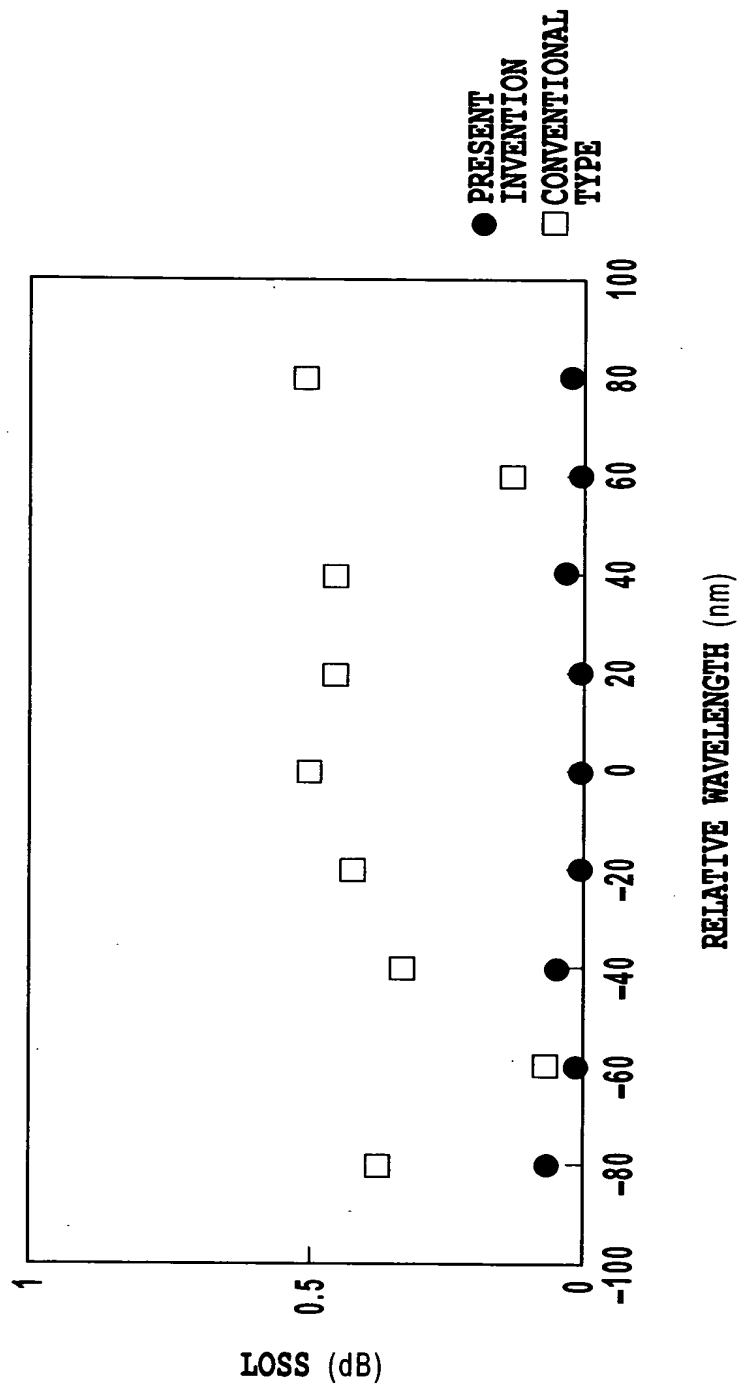


FIG.34

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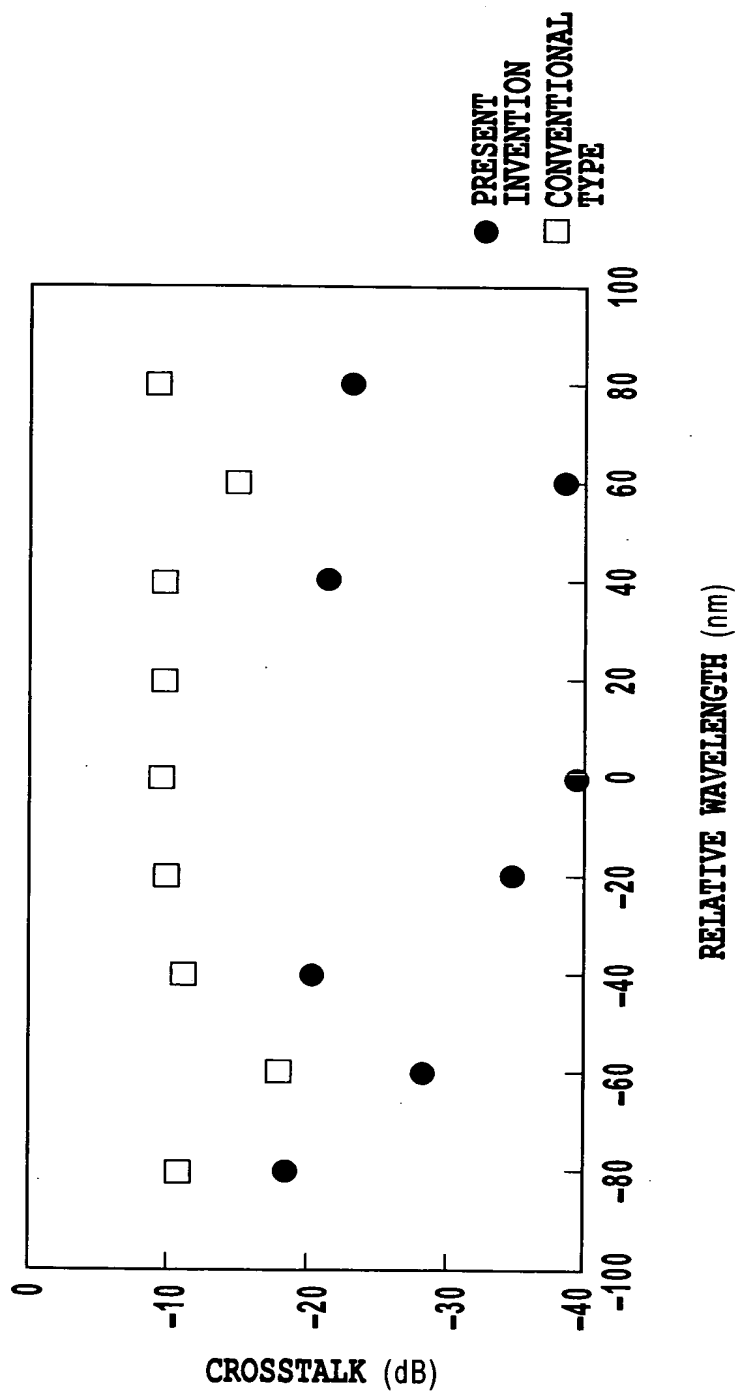


FIG.35

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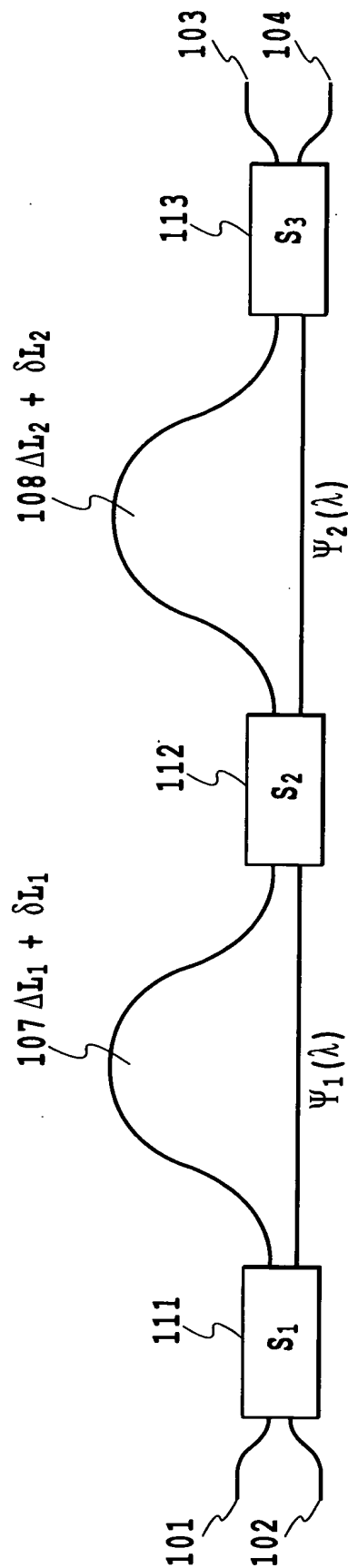


FIG.36

Title: OPTICAL MULTI/DEMULTIPLEXING CIRCUIT
EQUIPPED WITH PHASE GENERATING DEVICE

Inventors: Takayuki Mizuno et al.

Docket No.: 14321.60

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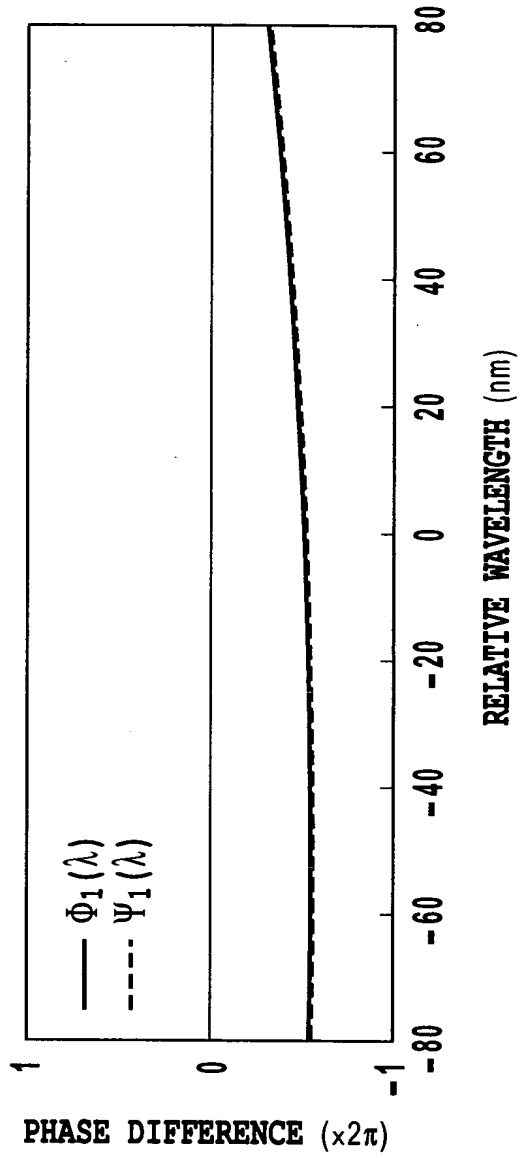


FIG. 37A

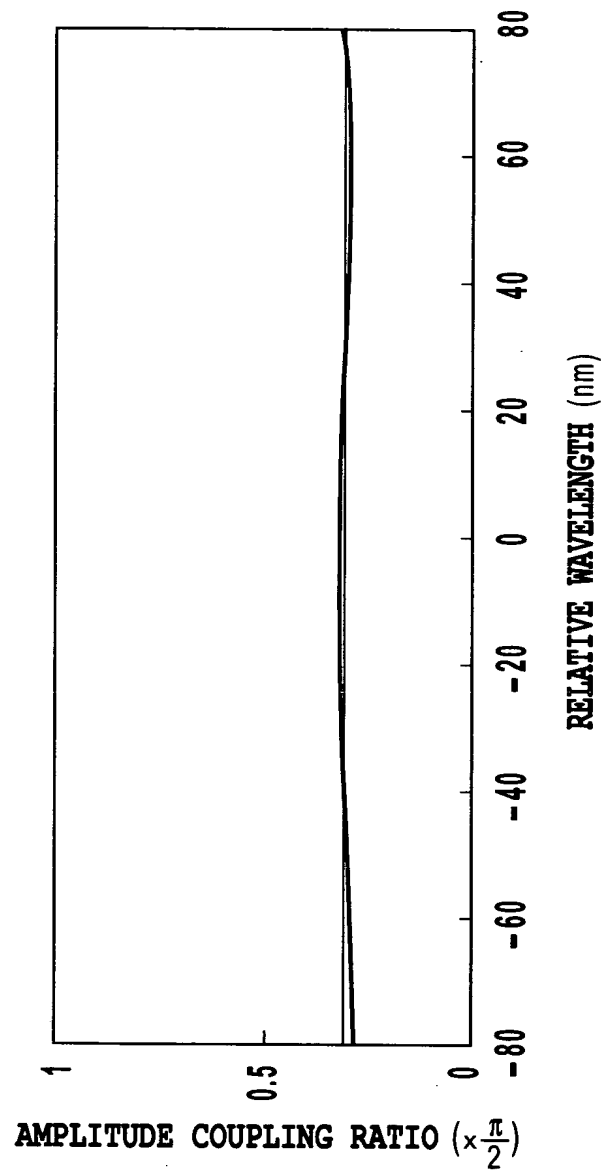


FIG. 37B

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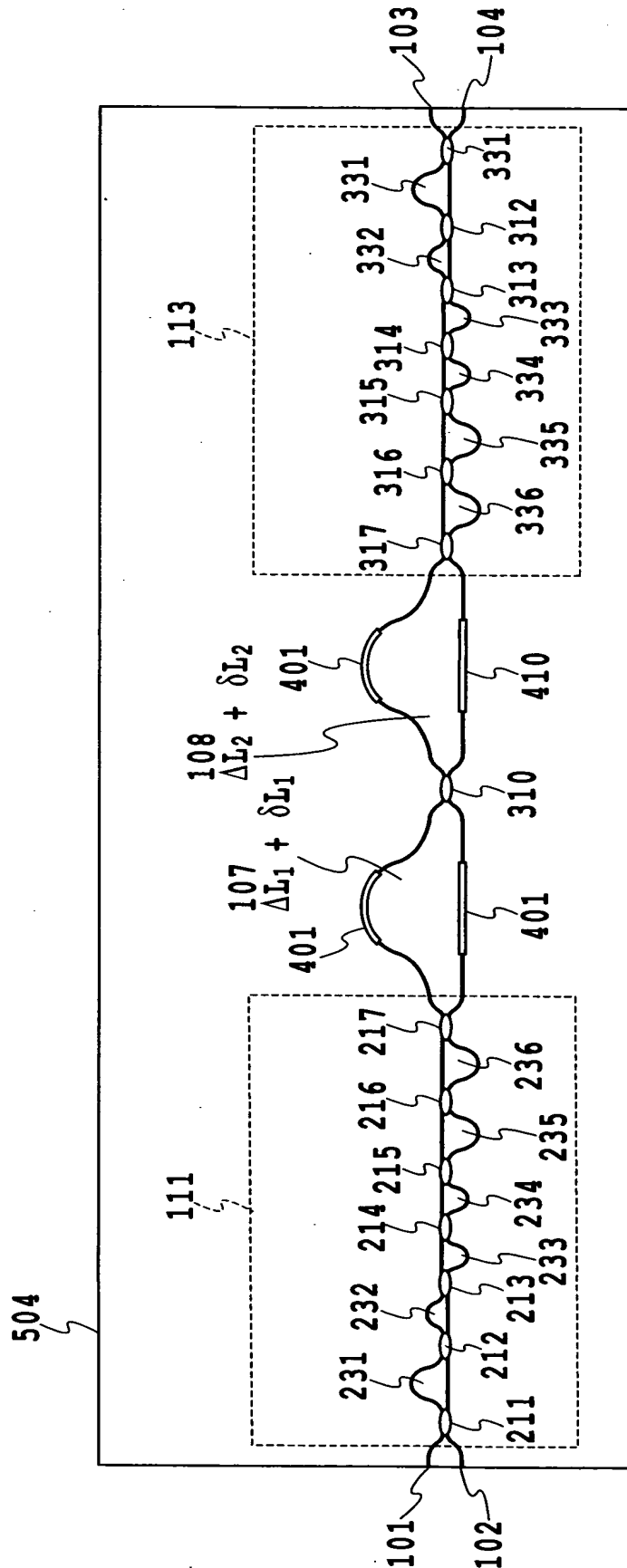


FIG.38

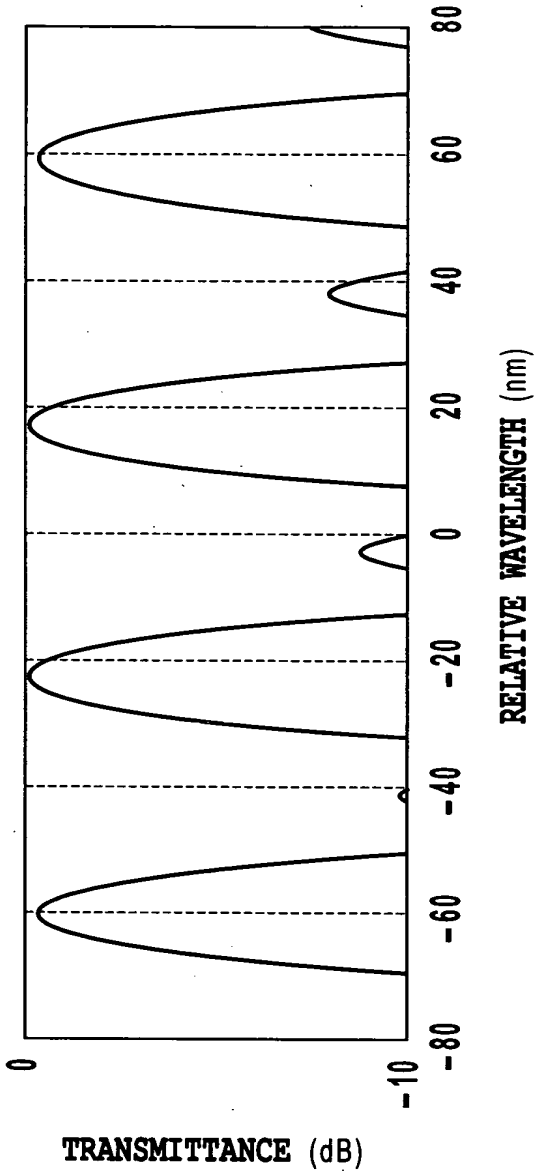


FIG. 39A

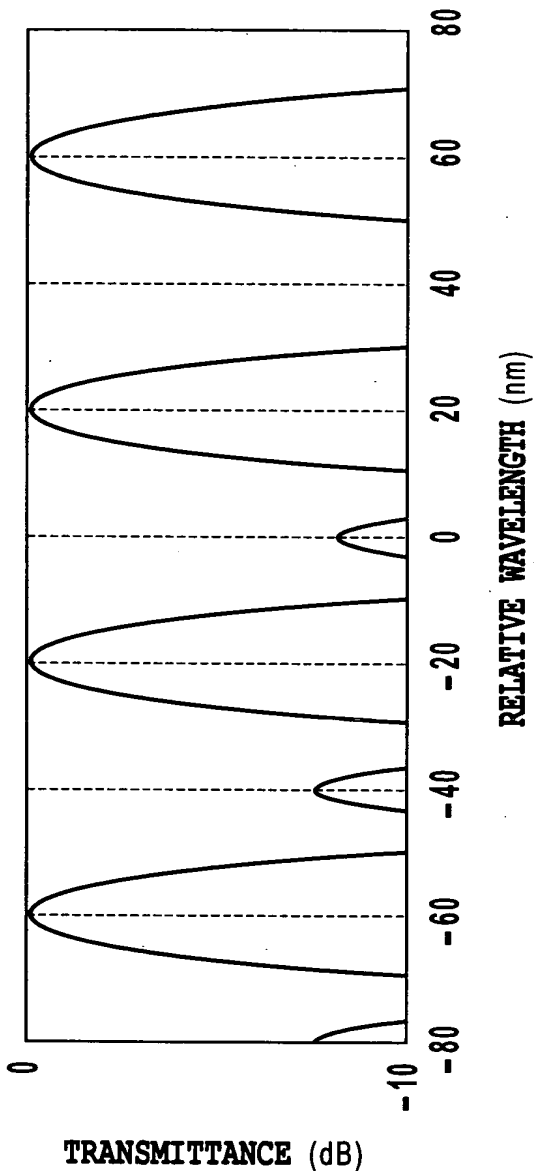


FIG. 39B

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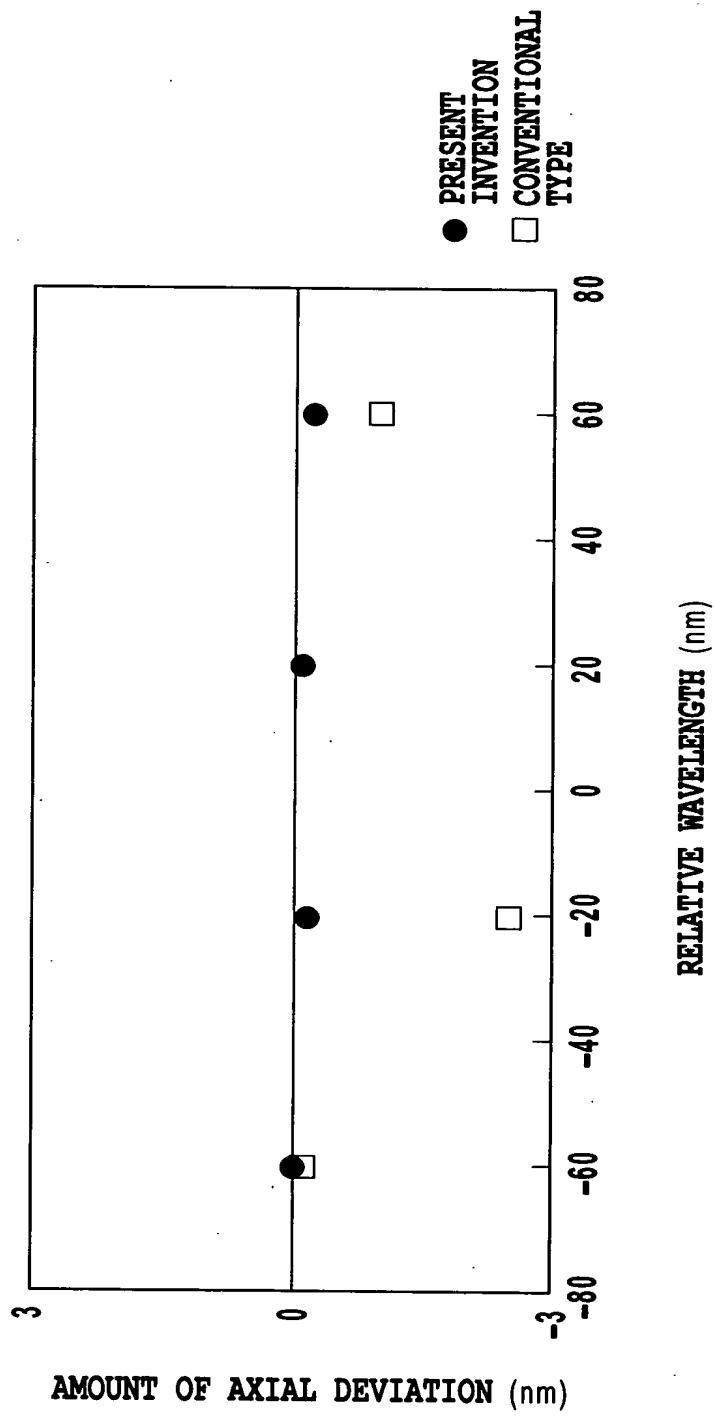


FIG.40

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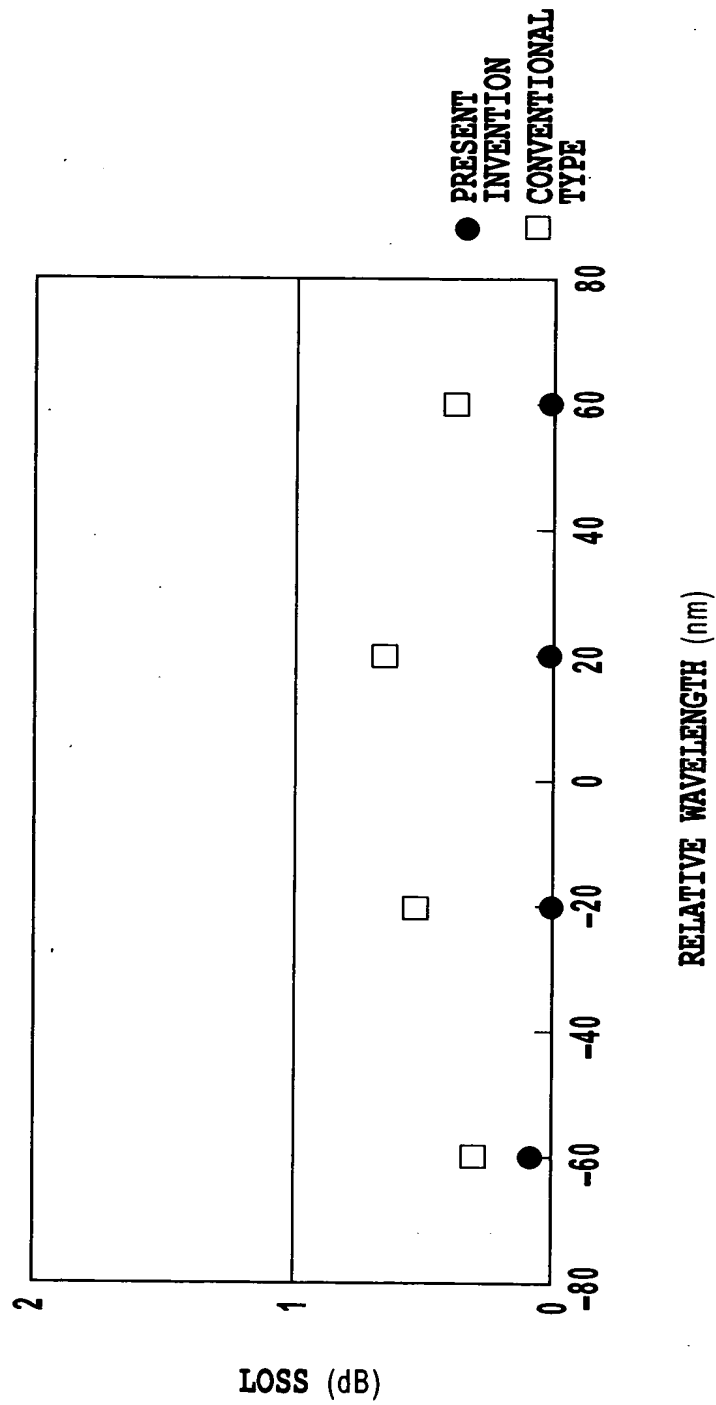


FIG.41

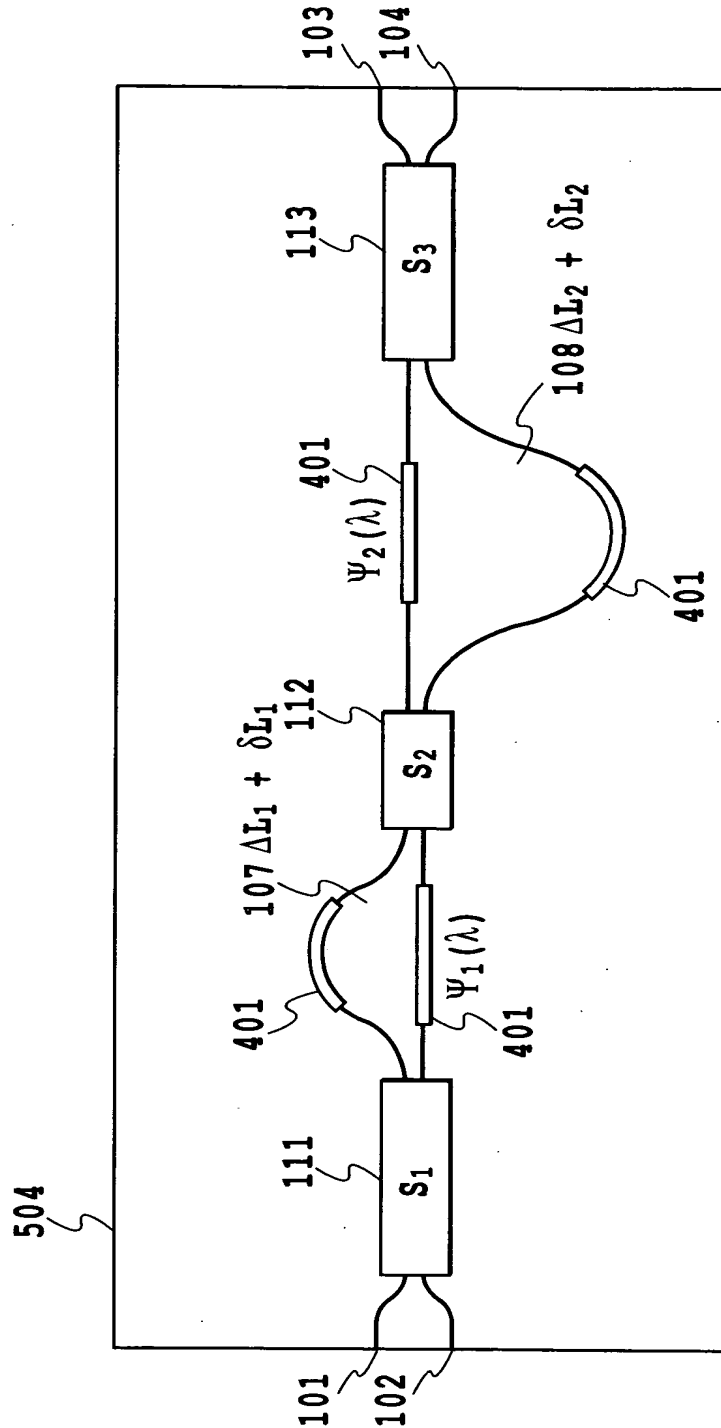


FIG. 42

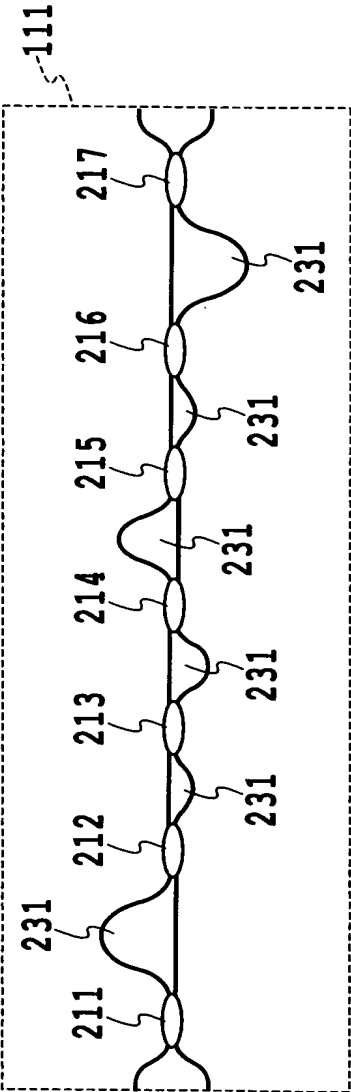


FIG. 43A

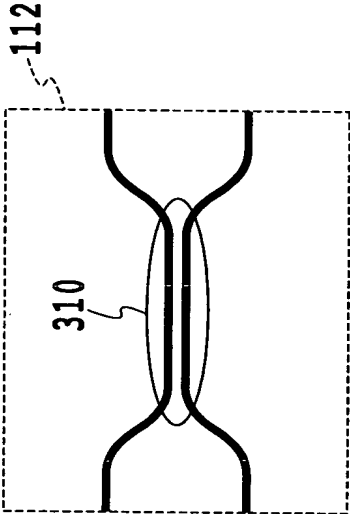


FIG. 43B

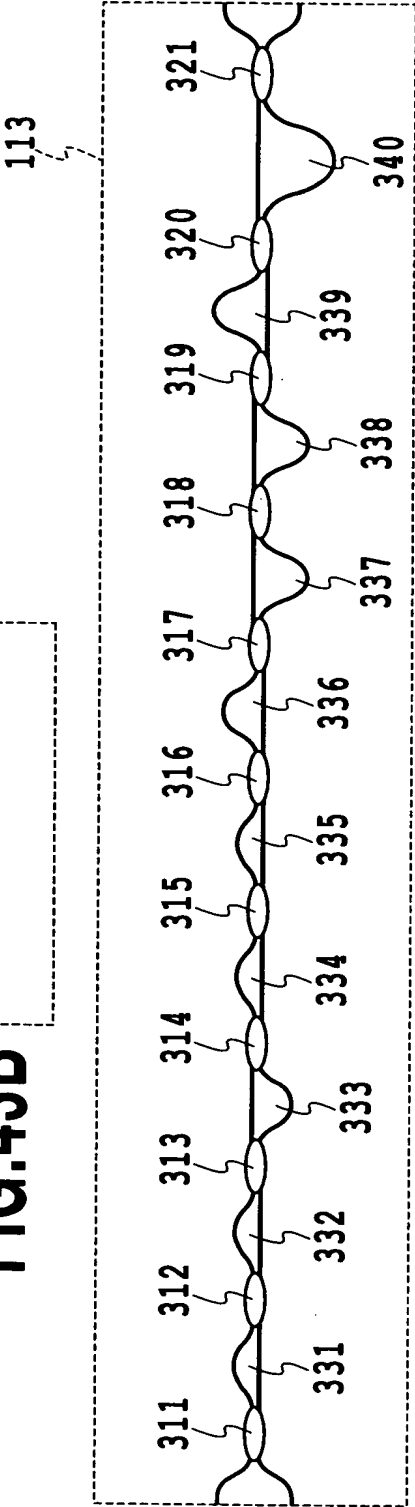


FIG. 43C

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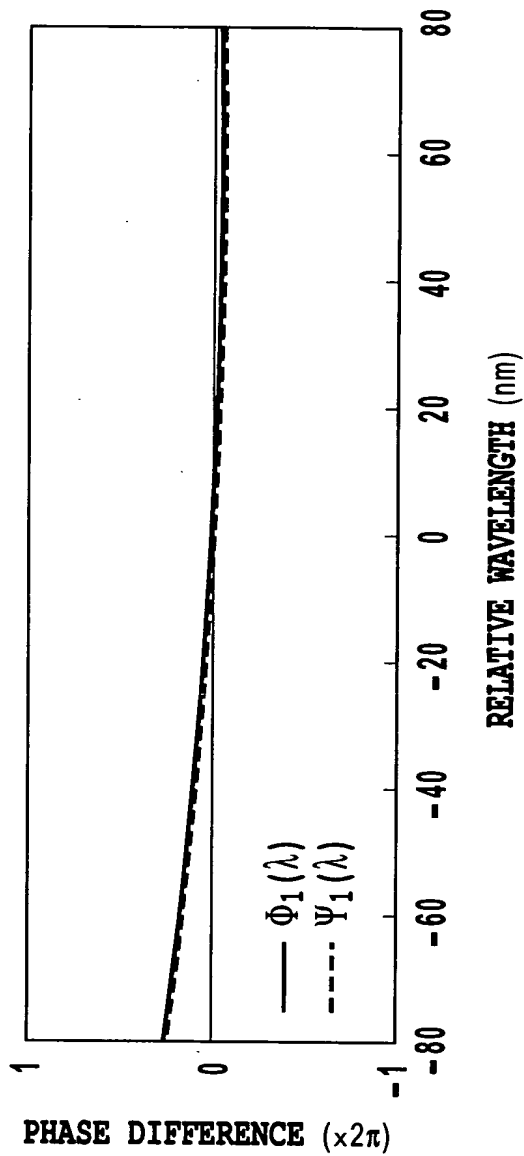


FIG. 44A

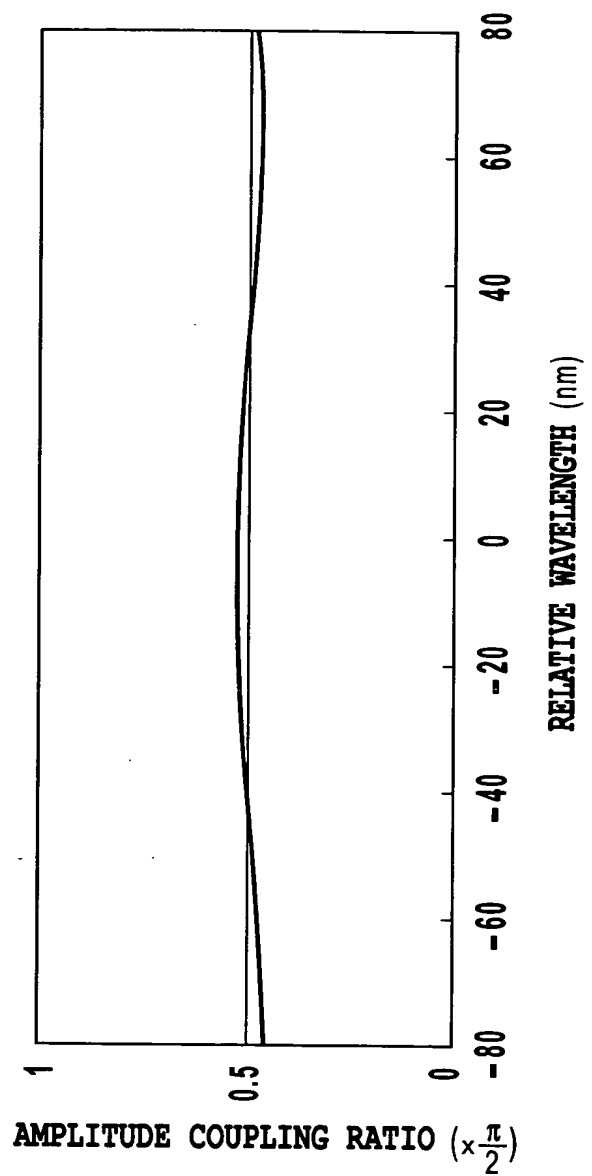


FIG. 44B

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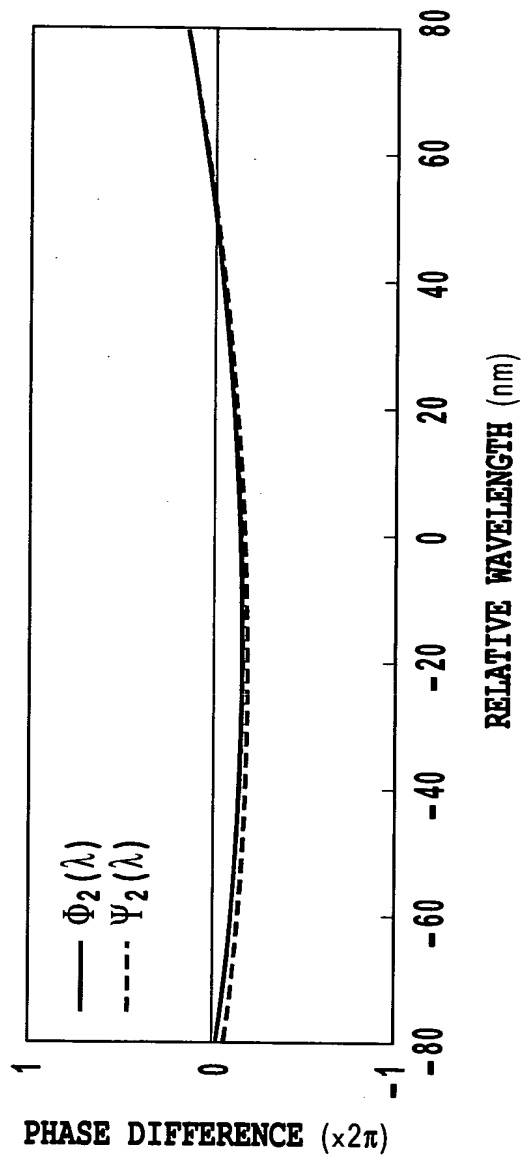


FIG. 45A

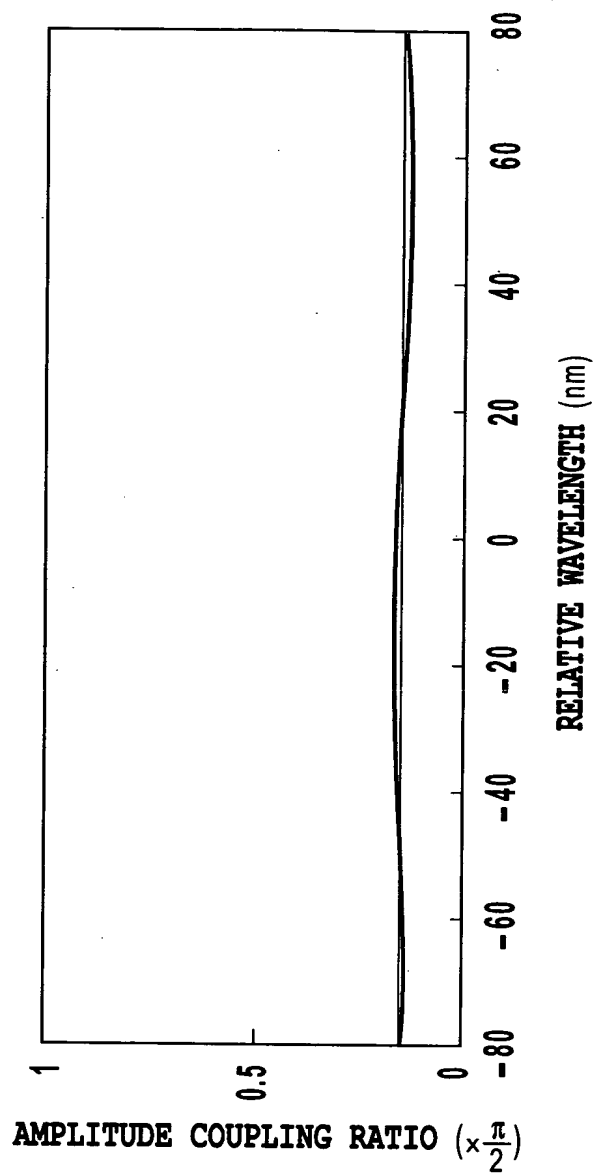


FIG. 45B

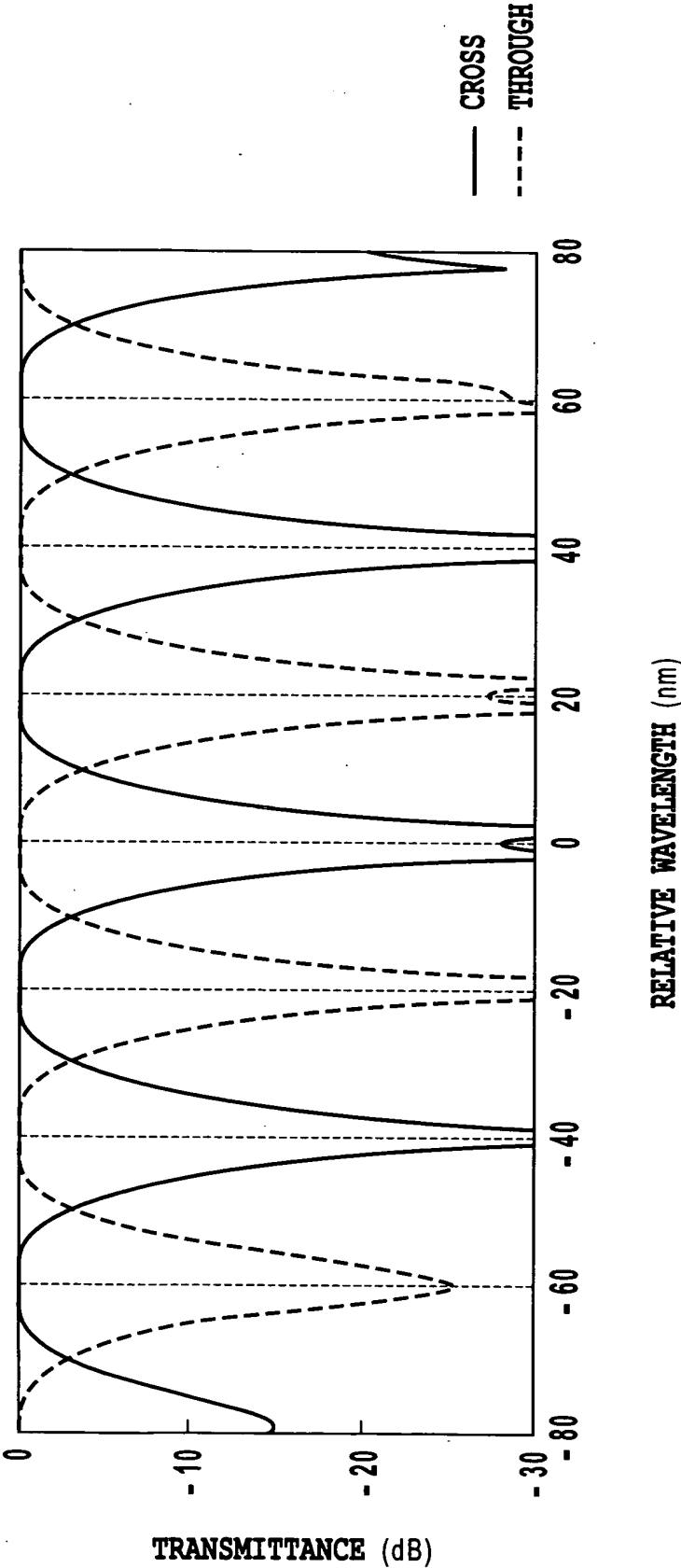


FIG.46

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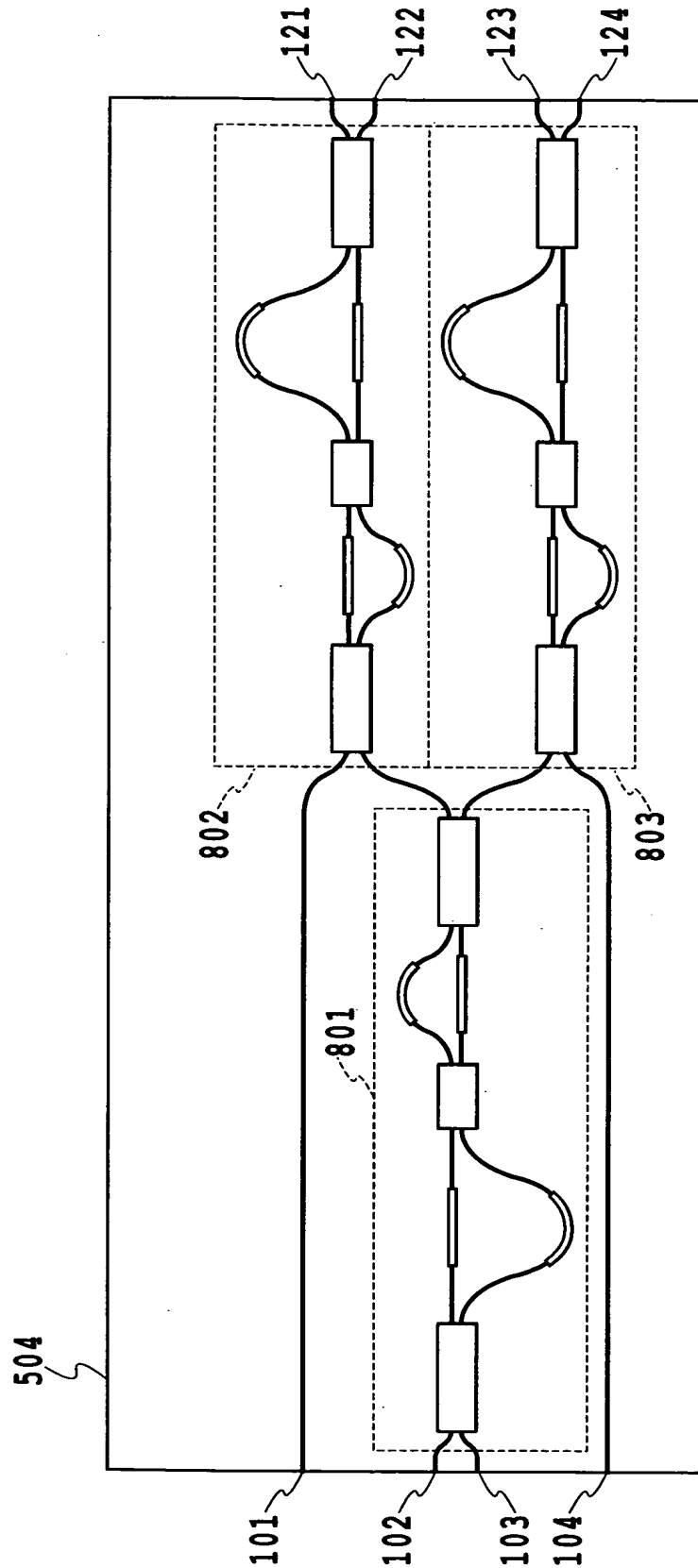


FIG.47

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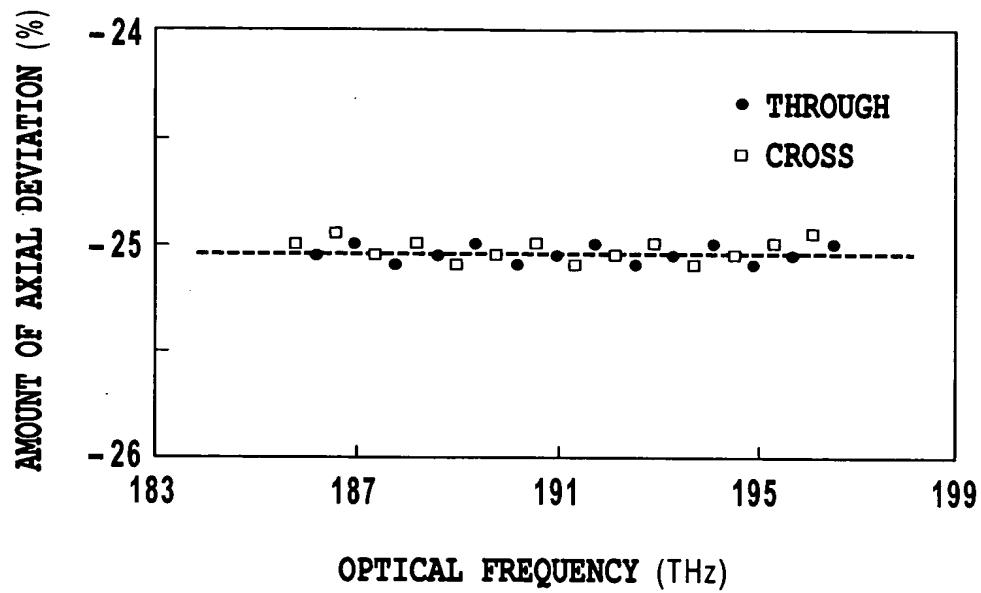


FIG.48A

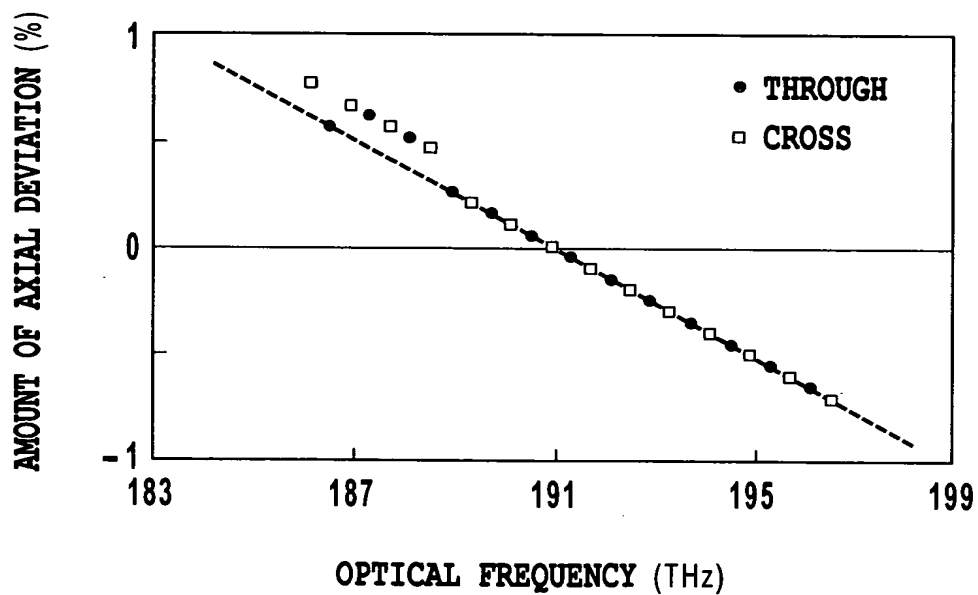


FIG.48B

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EQUIPPED WITH PHASE GENERATING DEVICE

Inventors: Takayuki Mizuno et al.

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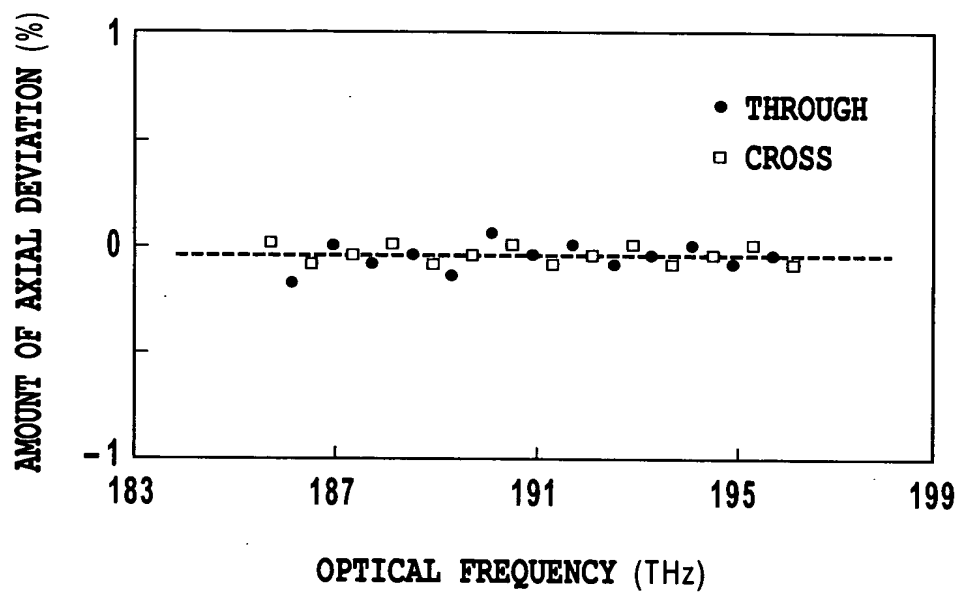


FIG.49

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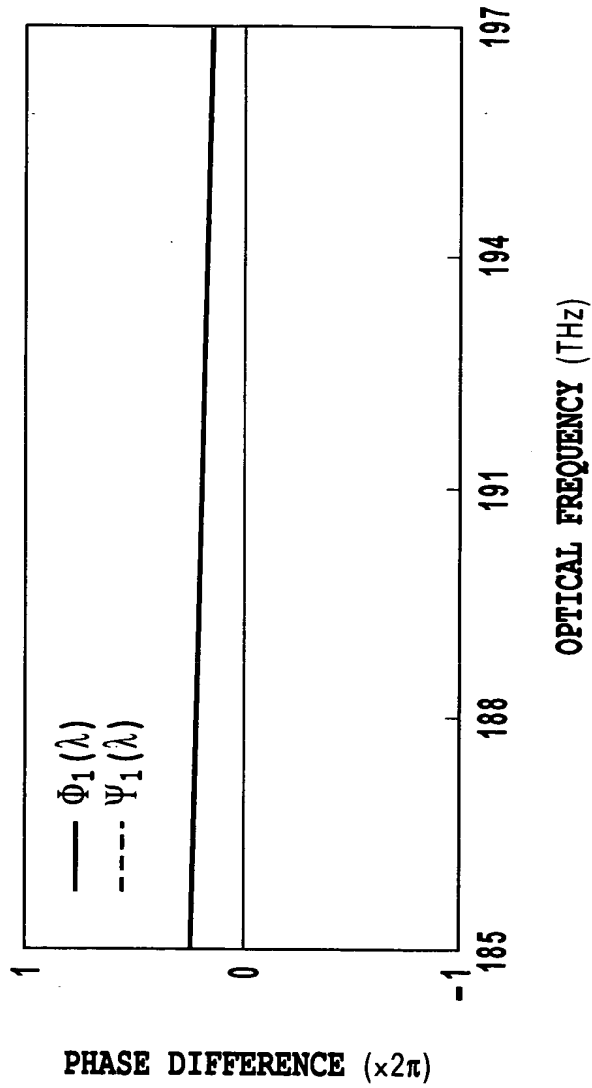


FIG. 50A

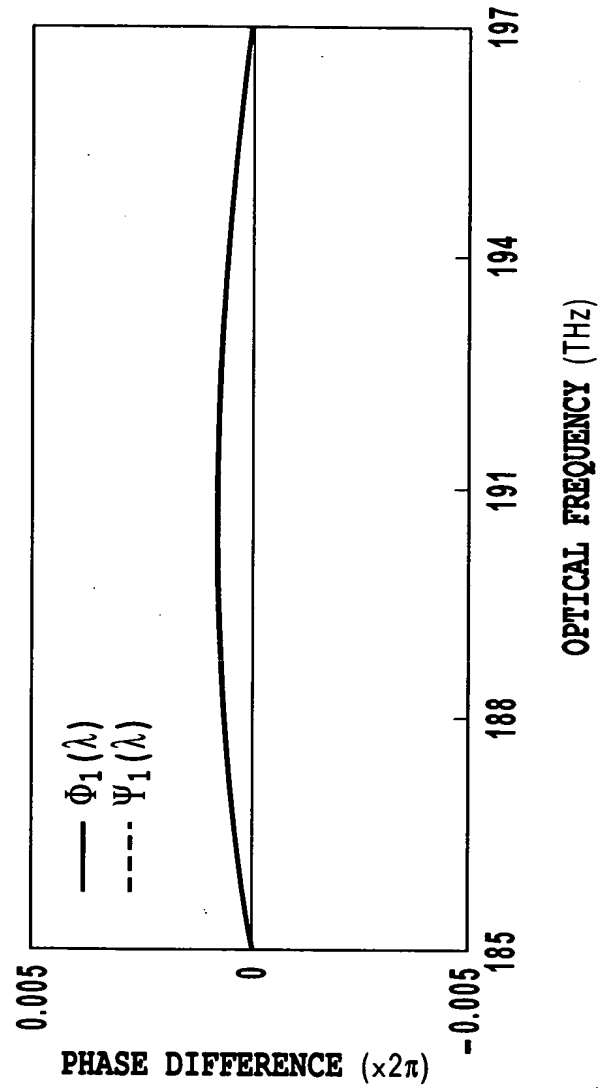


FIG. 50B

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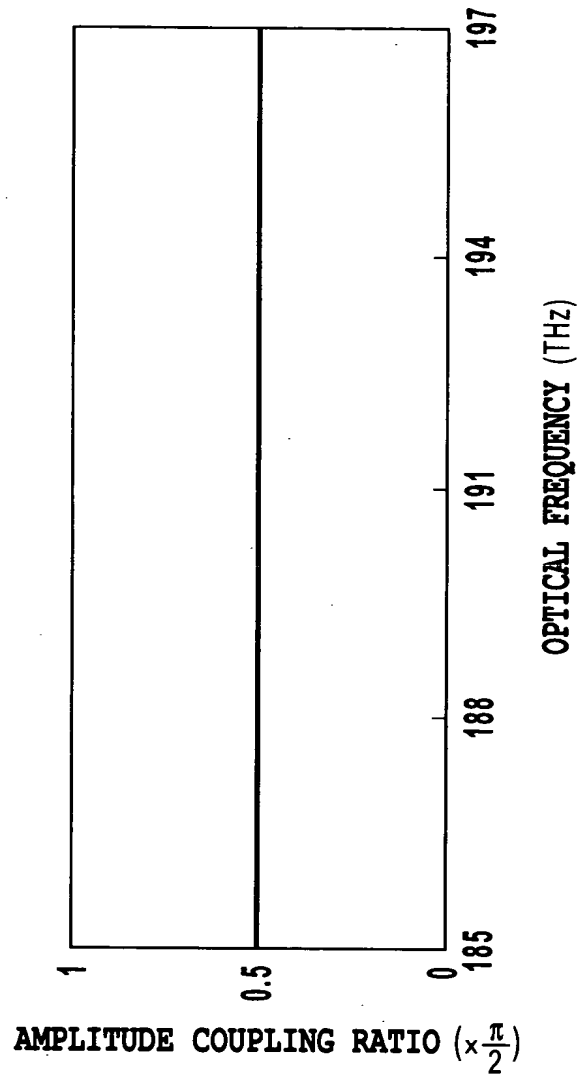


FIG.51

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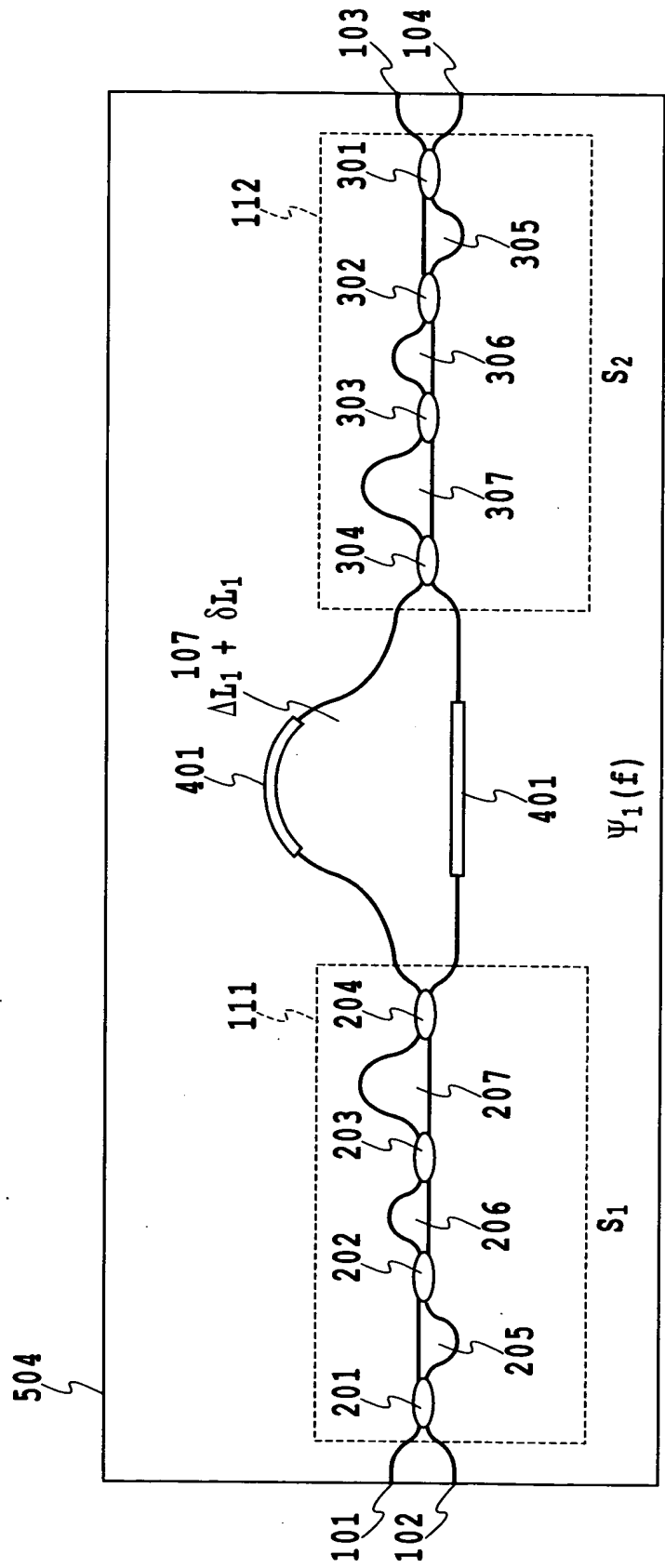


FIG.52

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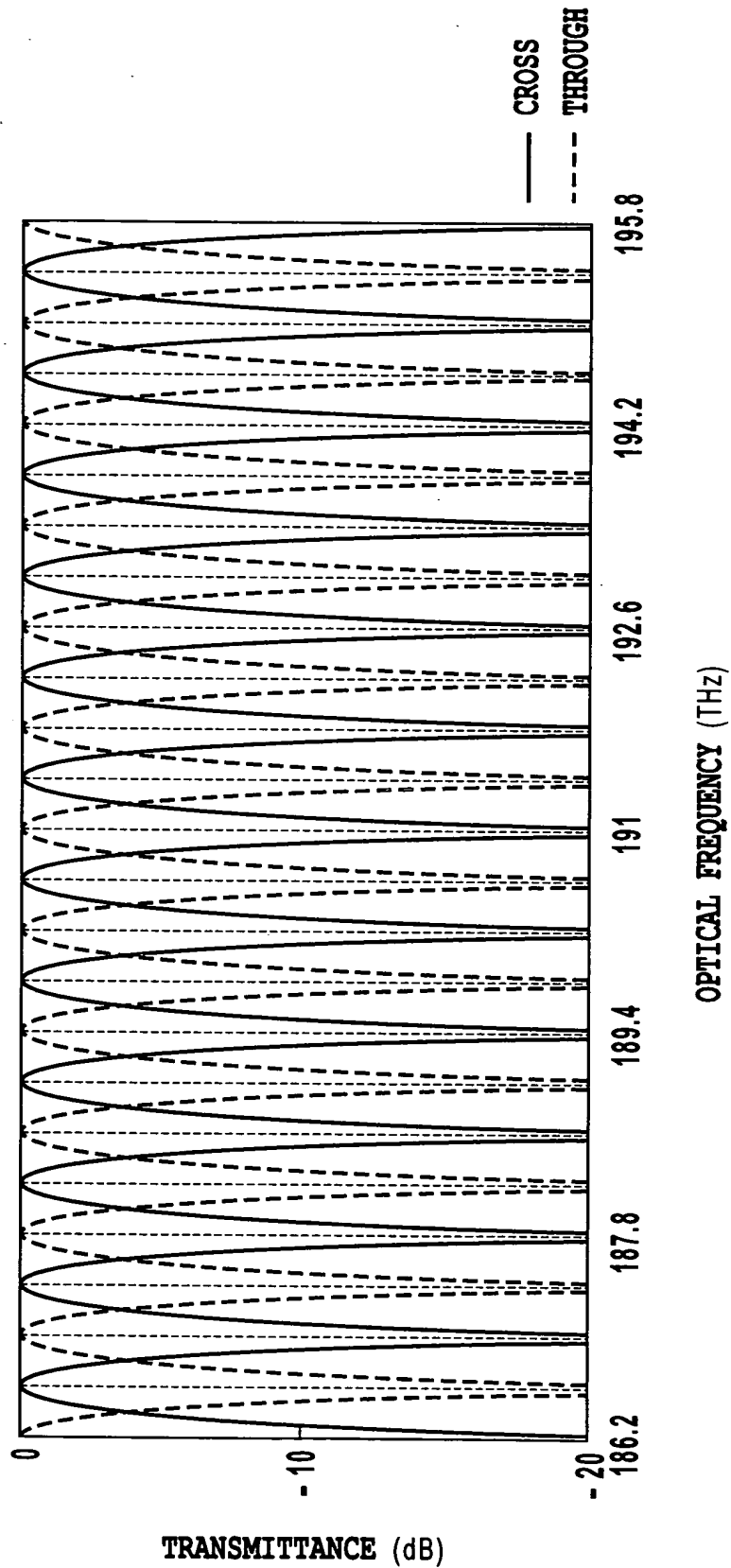


FIG.53

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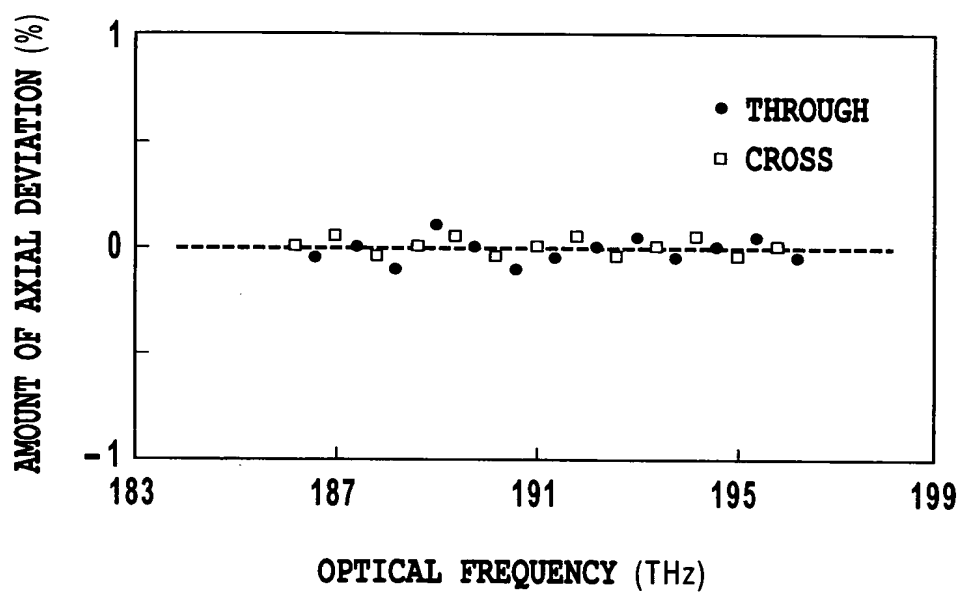


FIG.54

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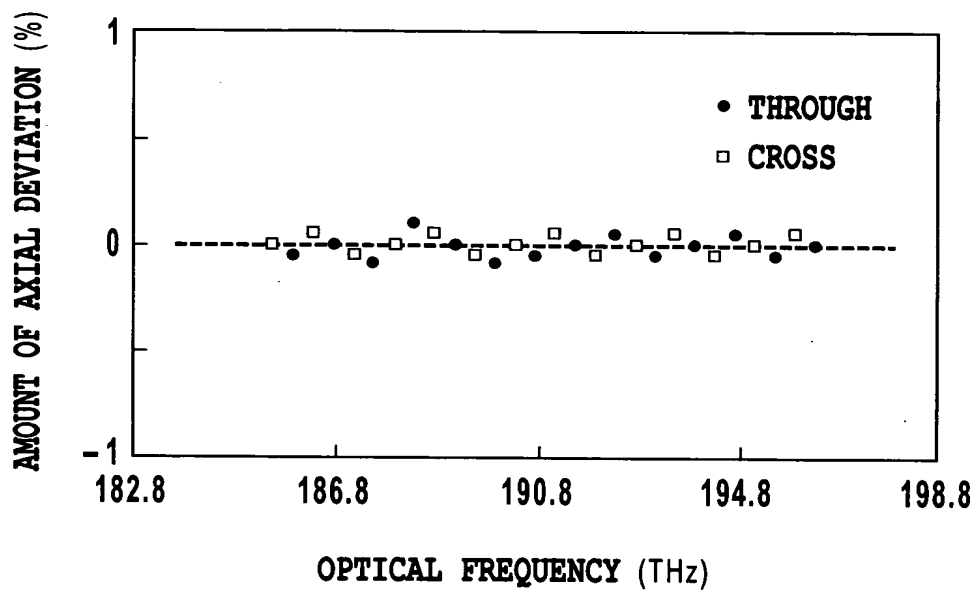


FIG.55

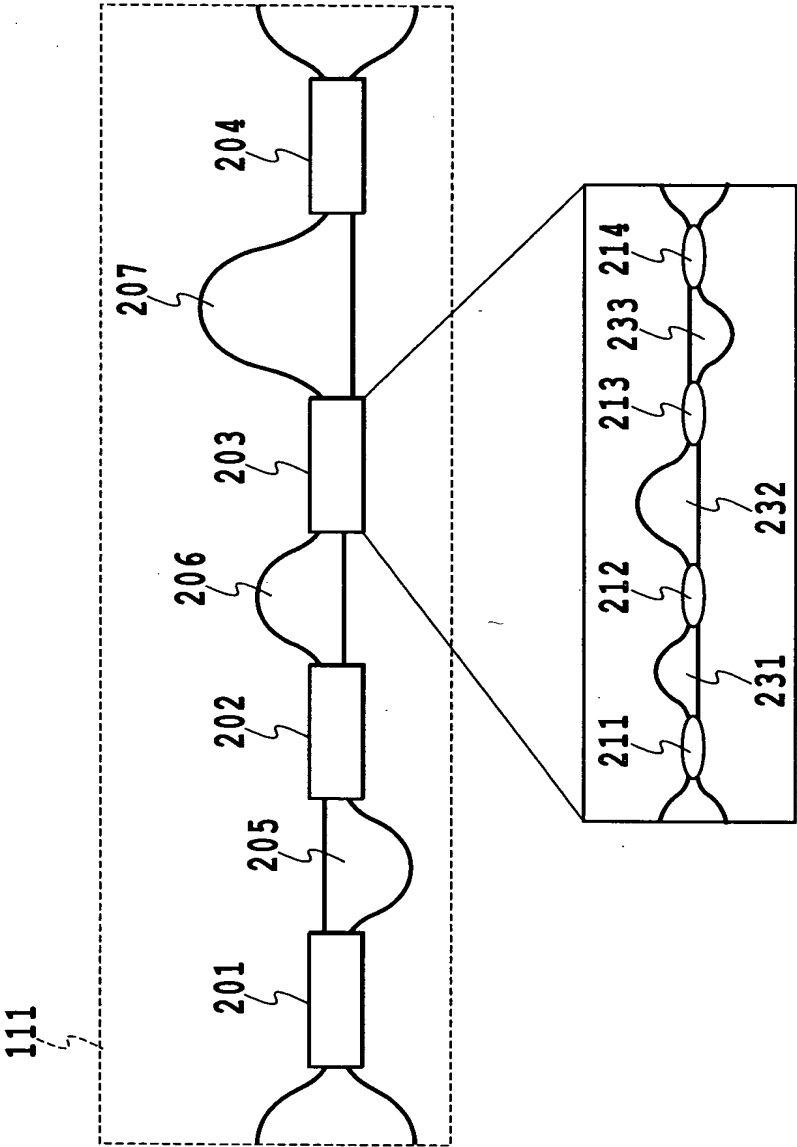


FIG.56

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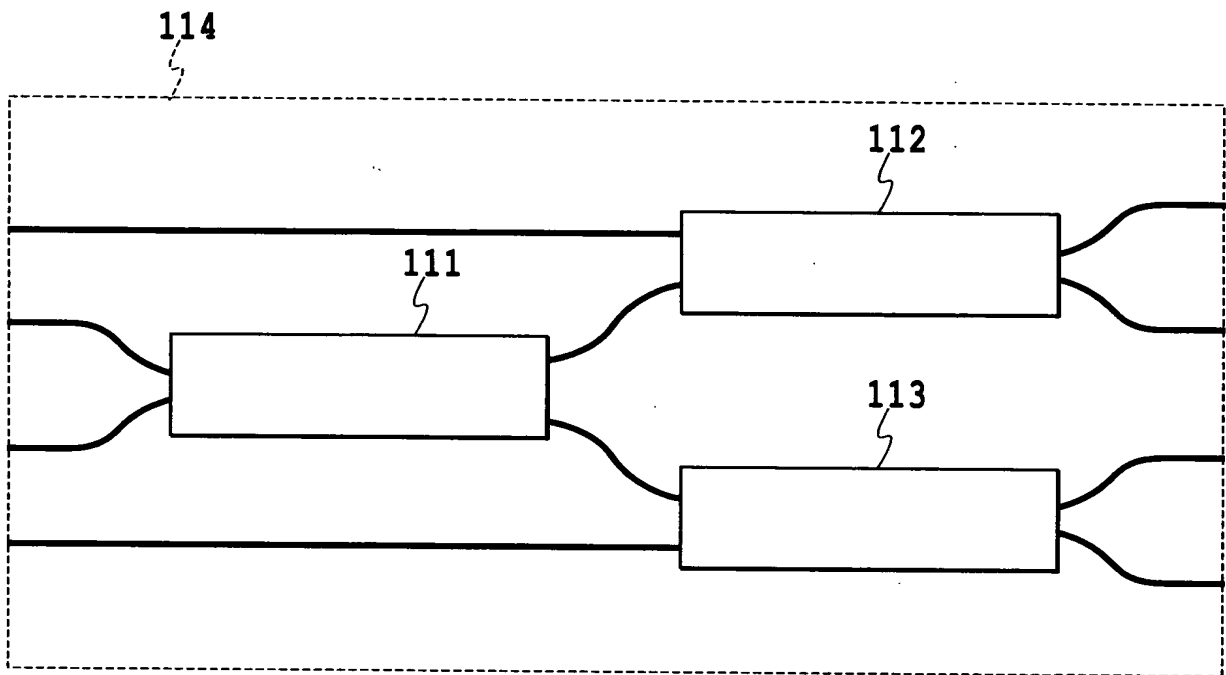


FIG.57

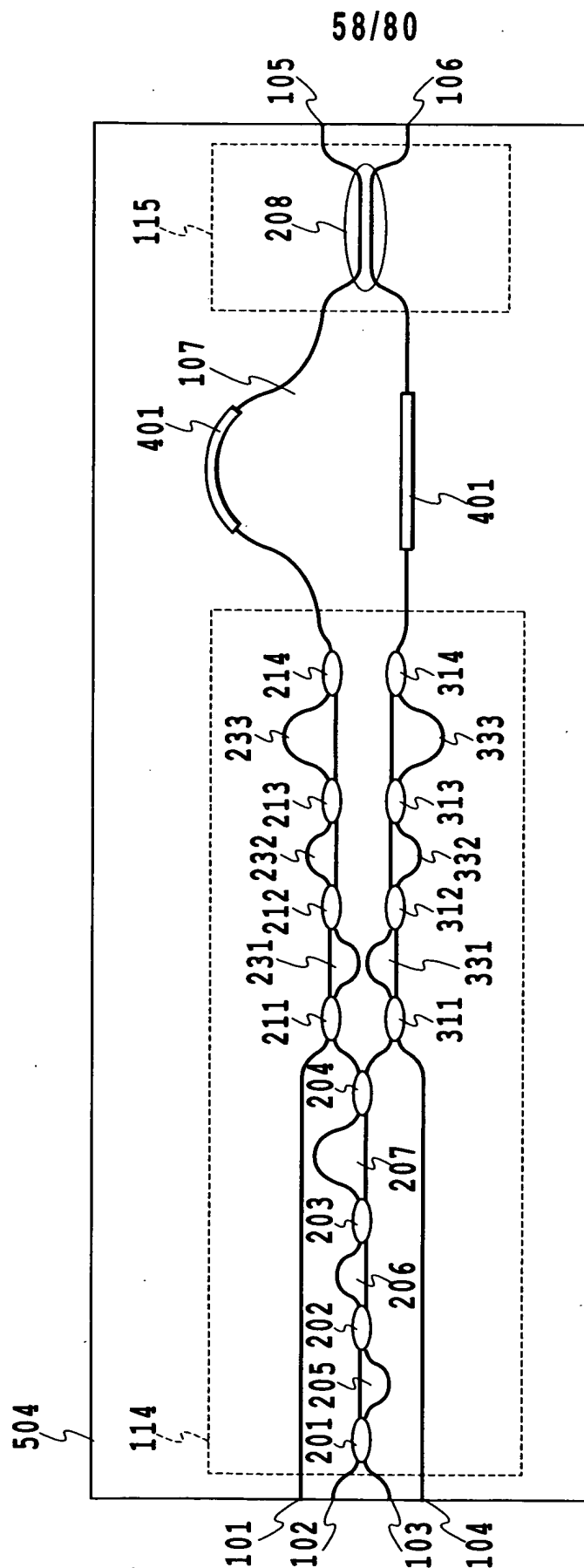


FIG.58

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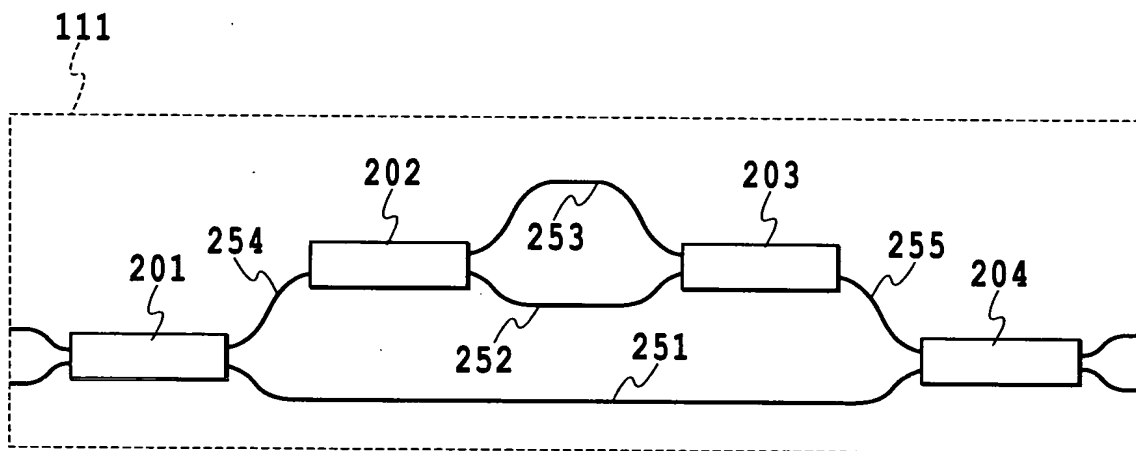
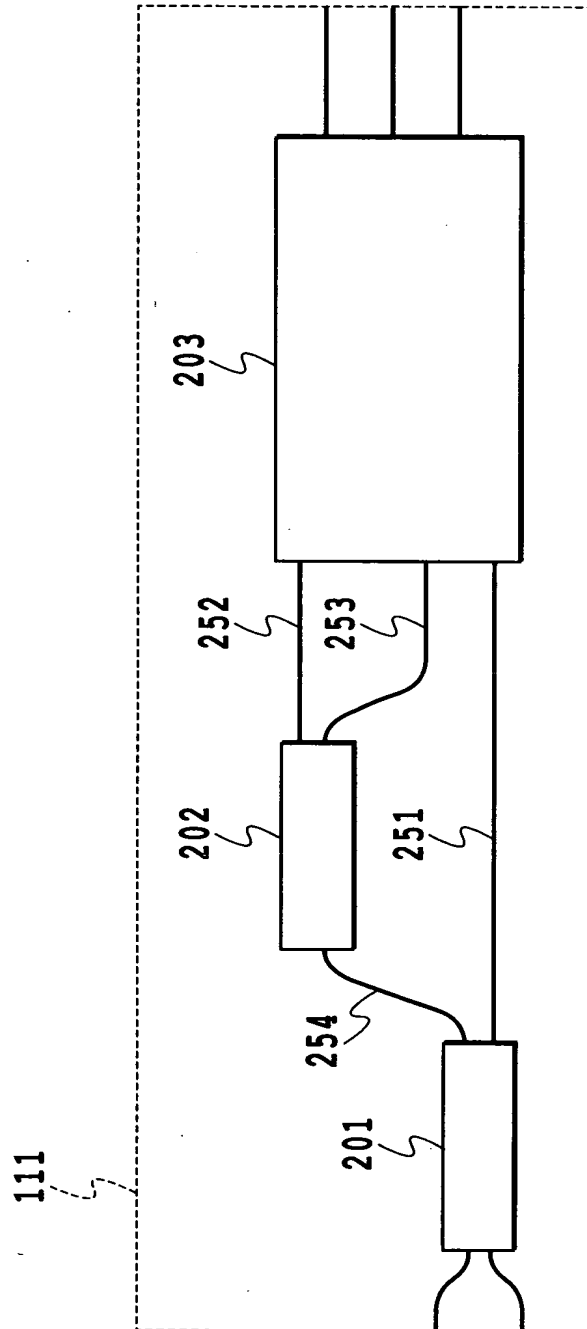


FIG.59



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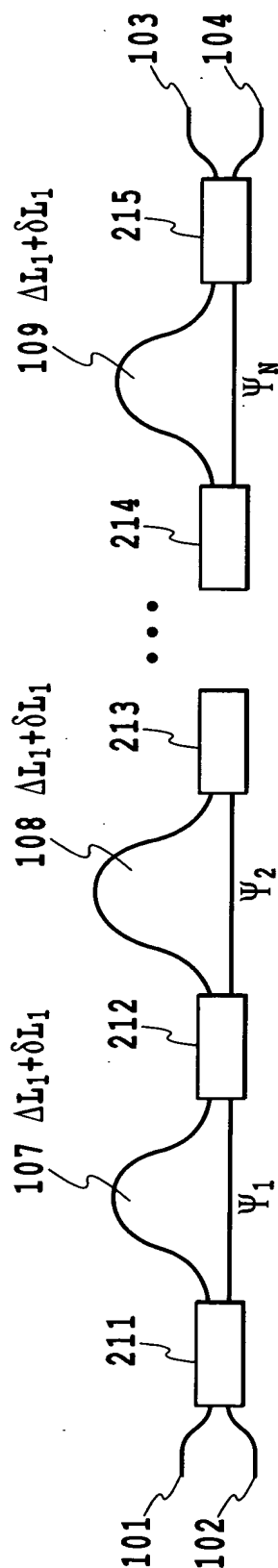


FIG.61

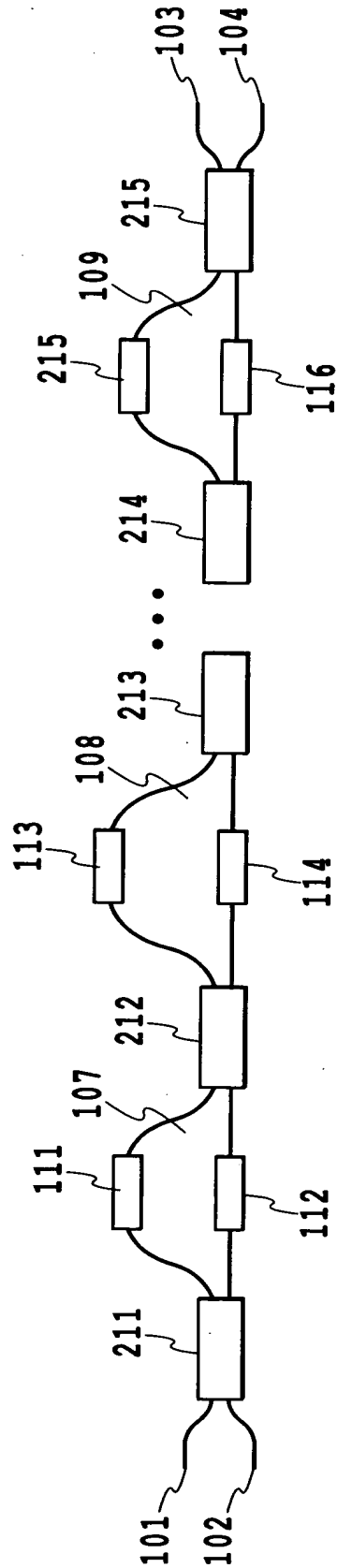


FIG. 62

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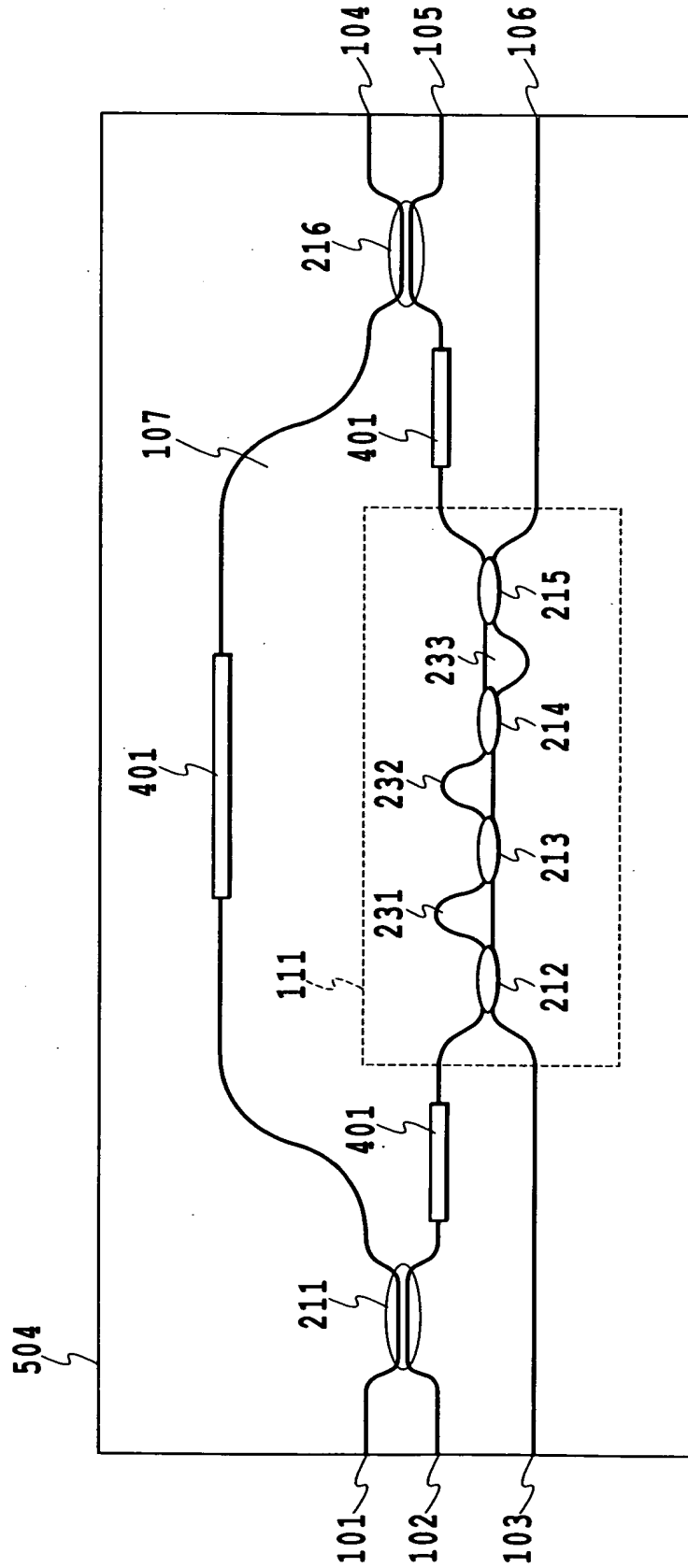


FIG.63

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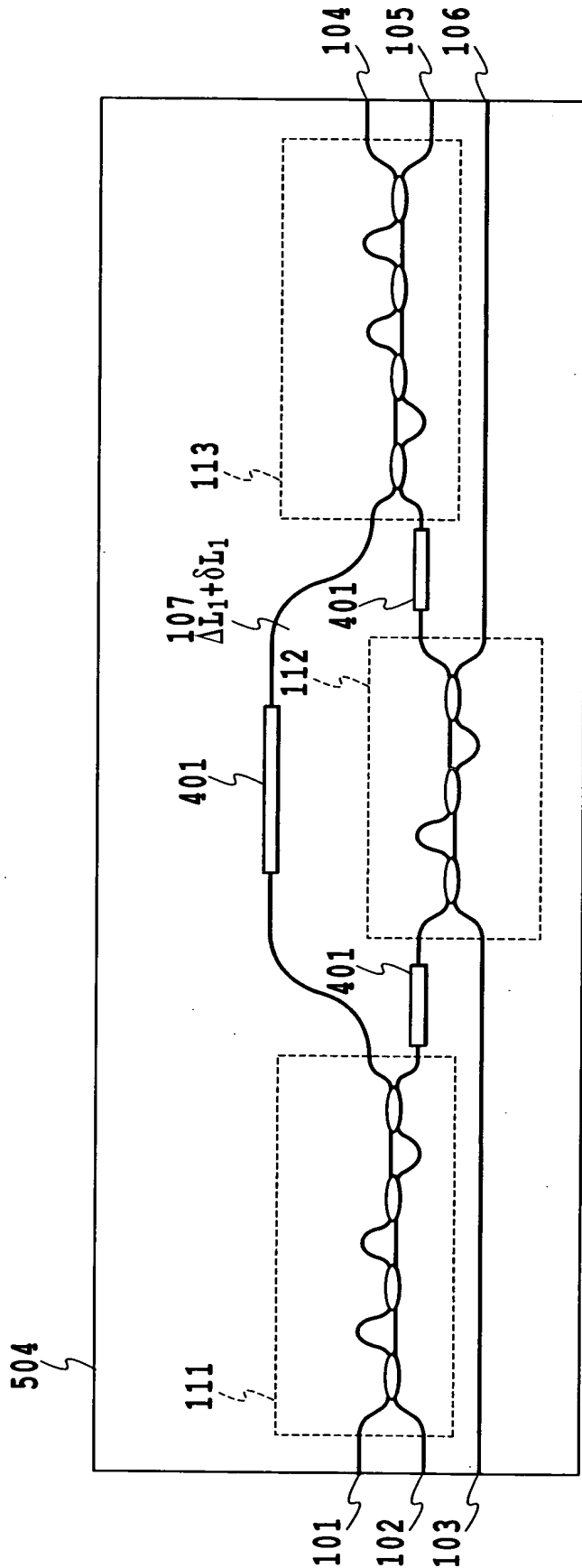


FIG.64

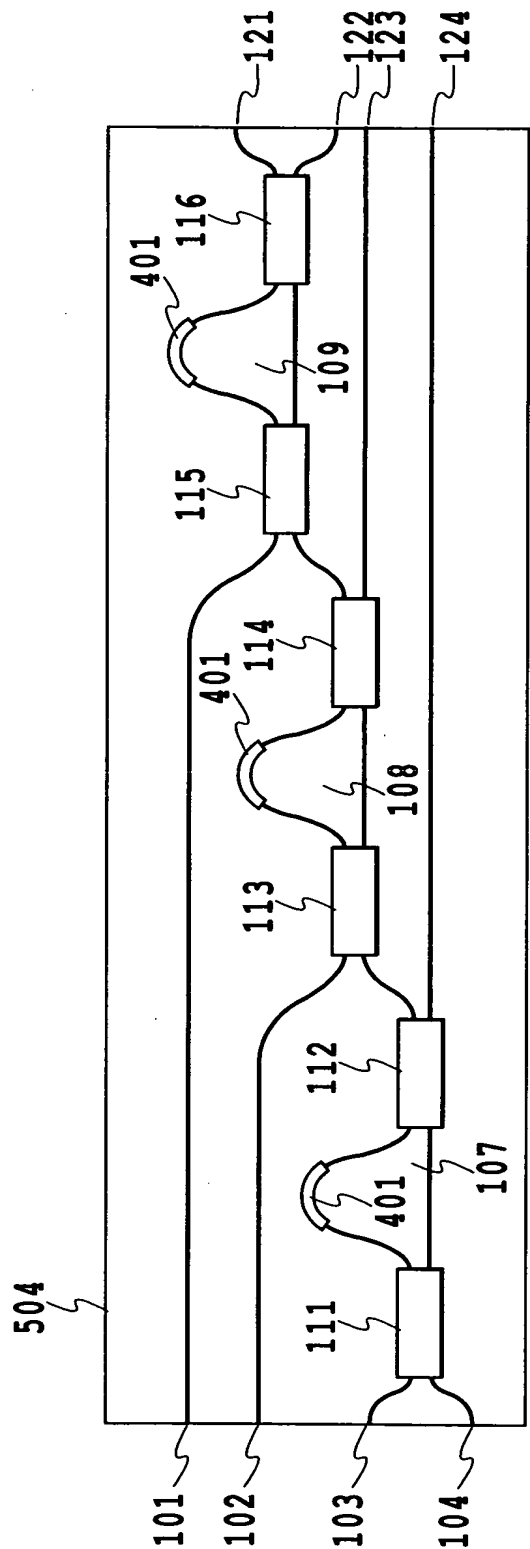


FIG. 65

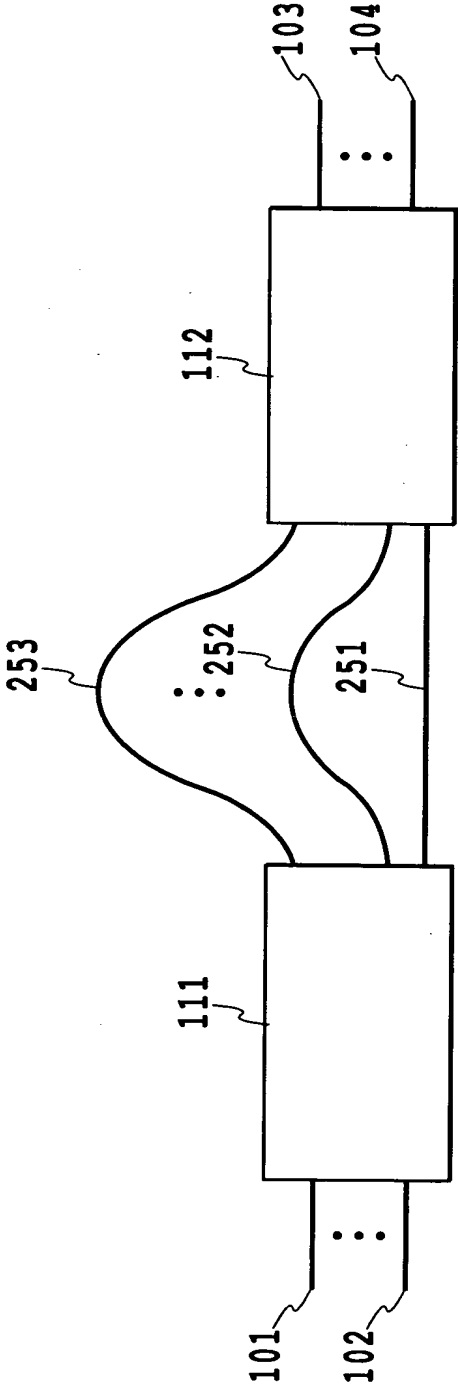


FIG.66

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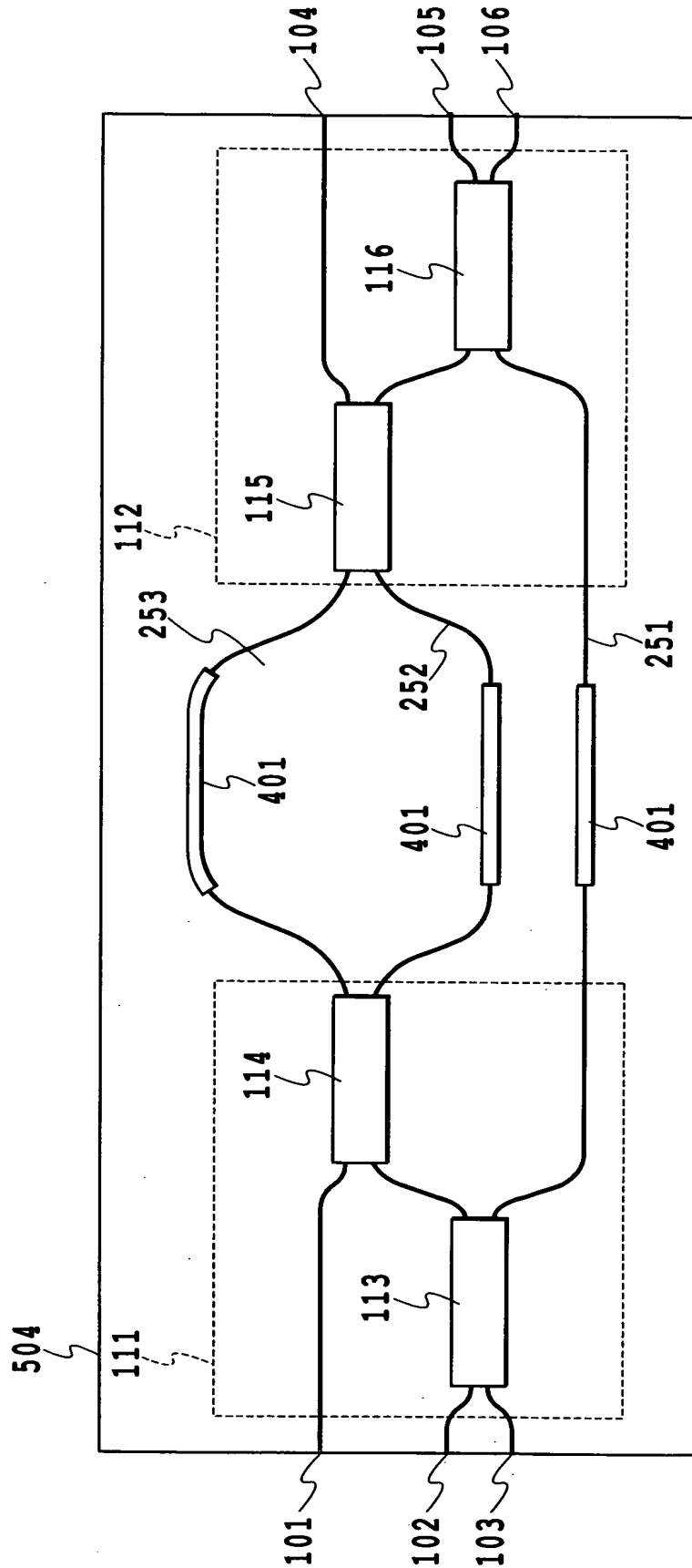


FIG.67

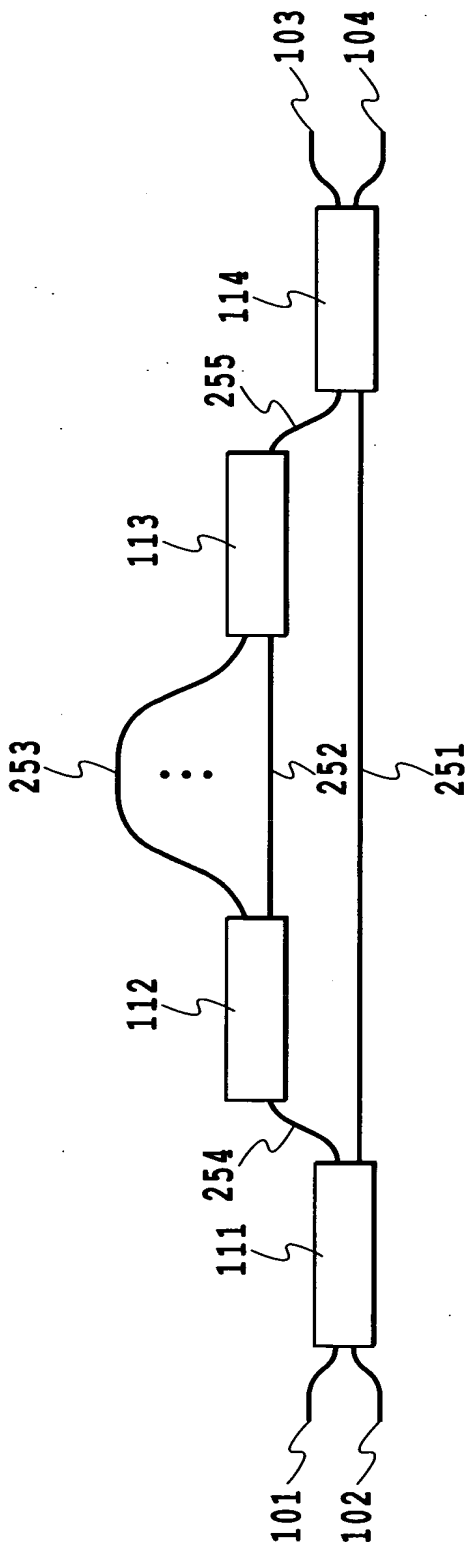


FIG.68

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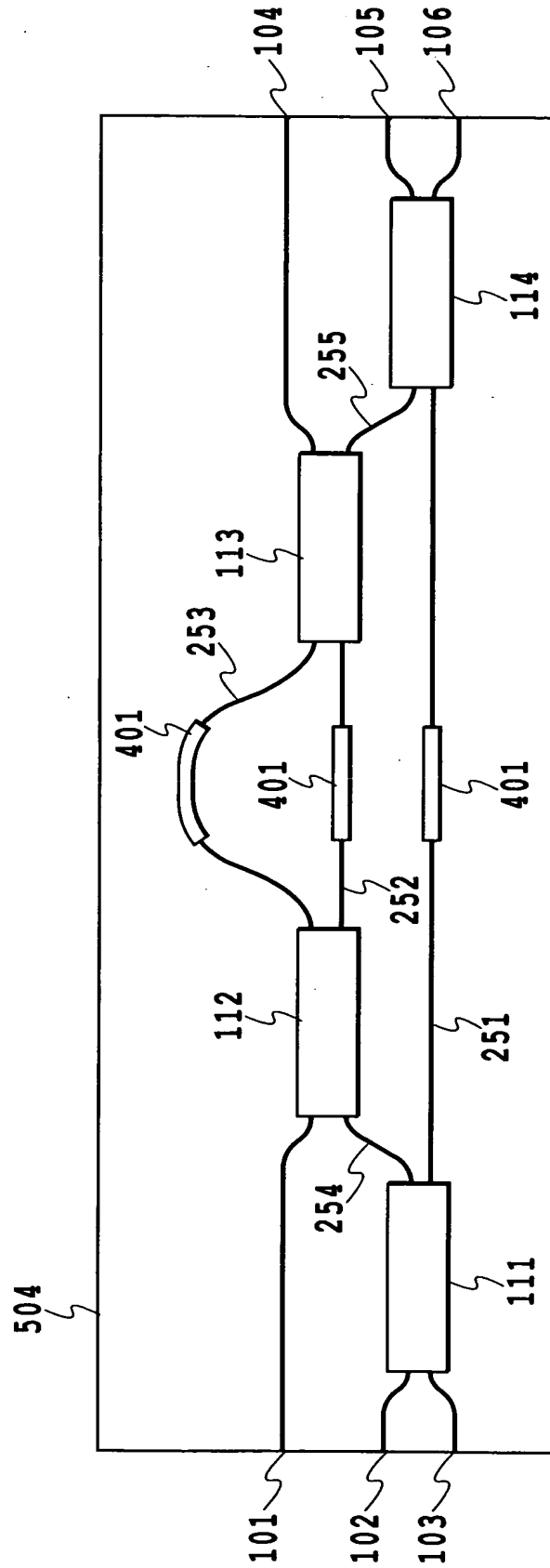


FIG.69

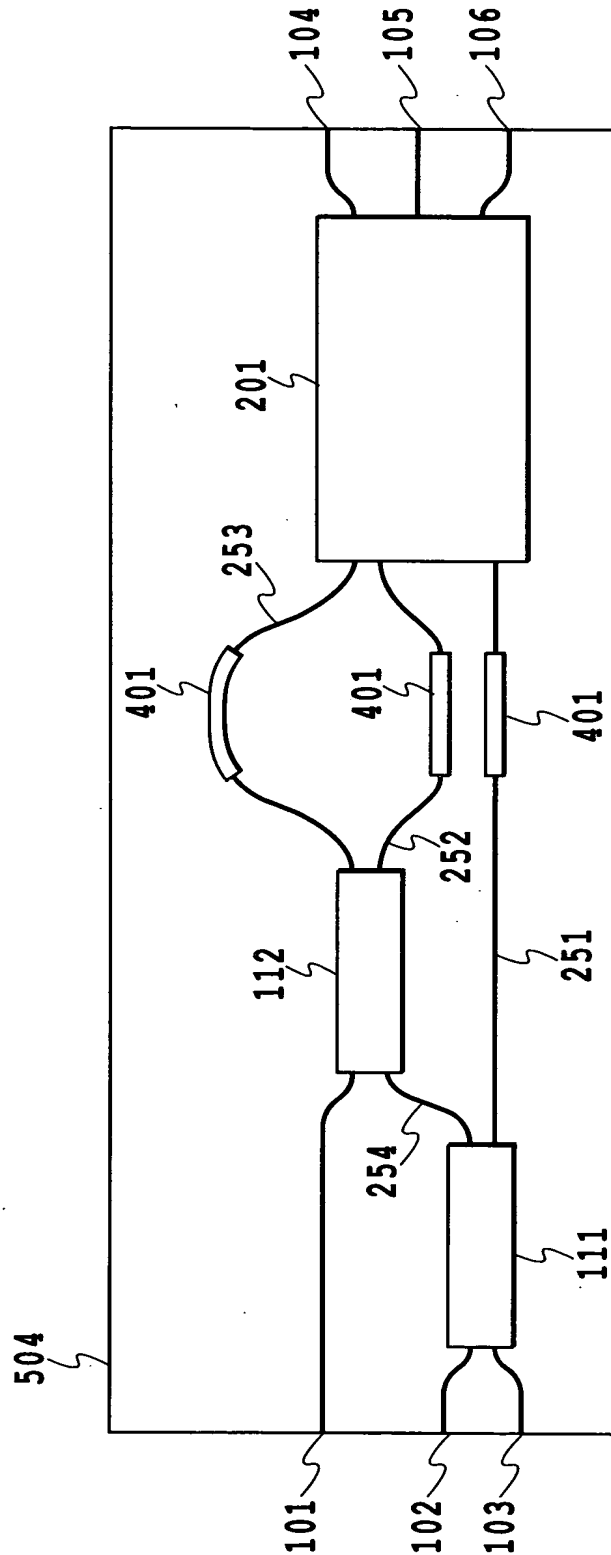


FIG.70

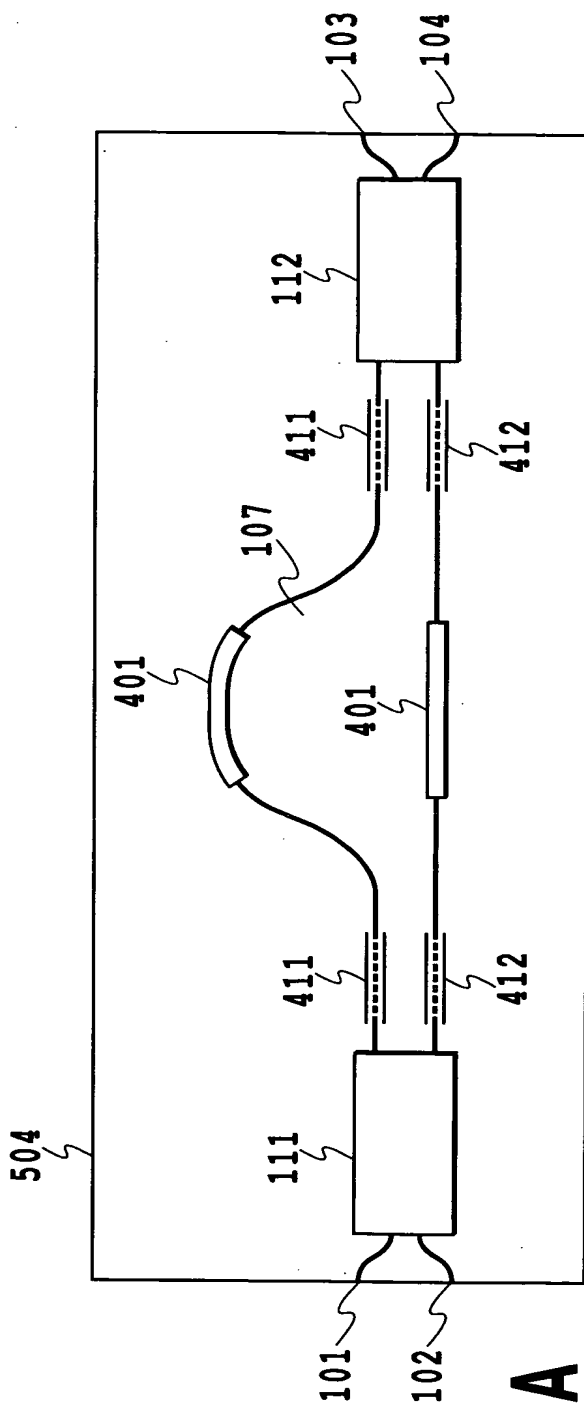


FIG. 71A

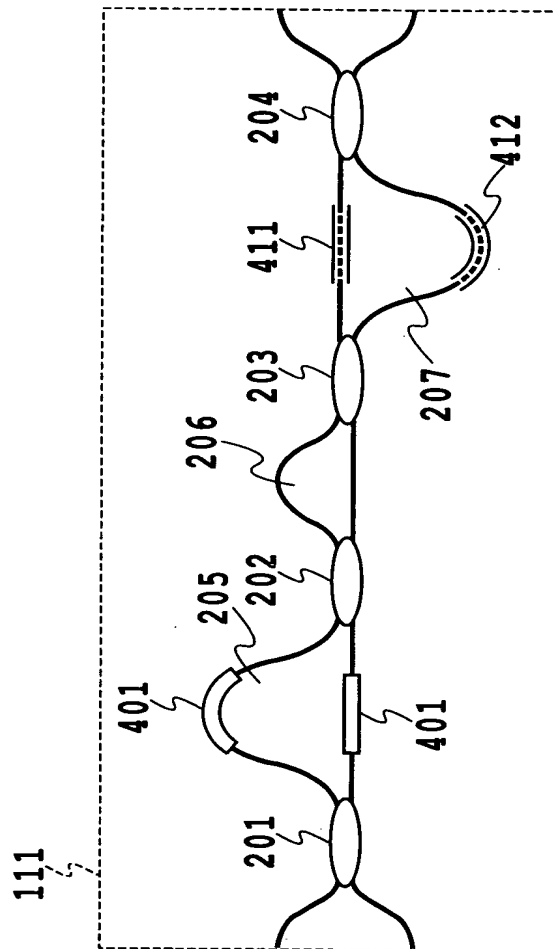


FIG. 71B

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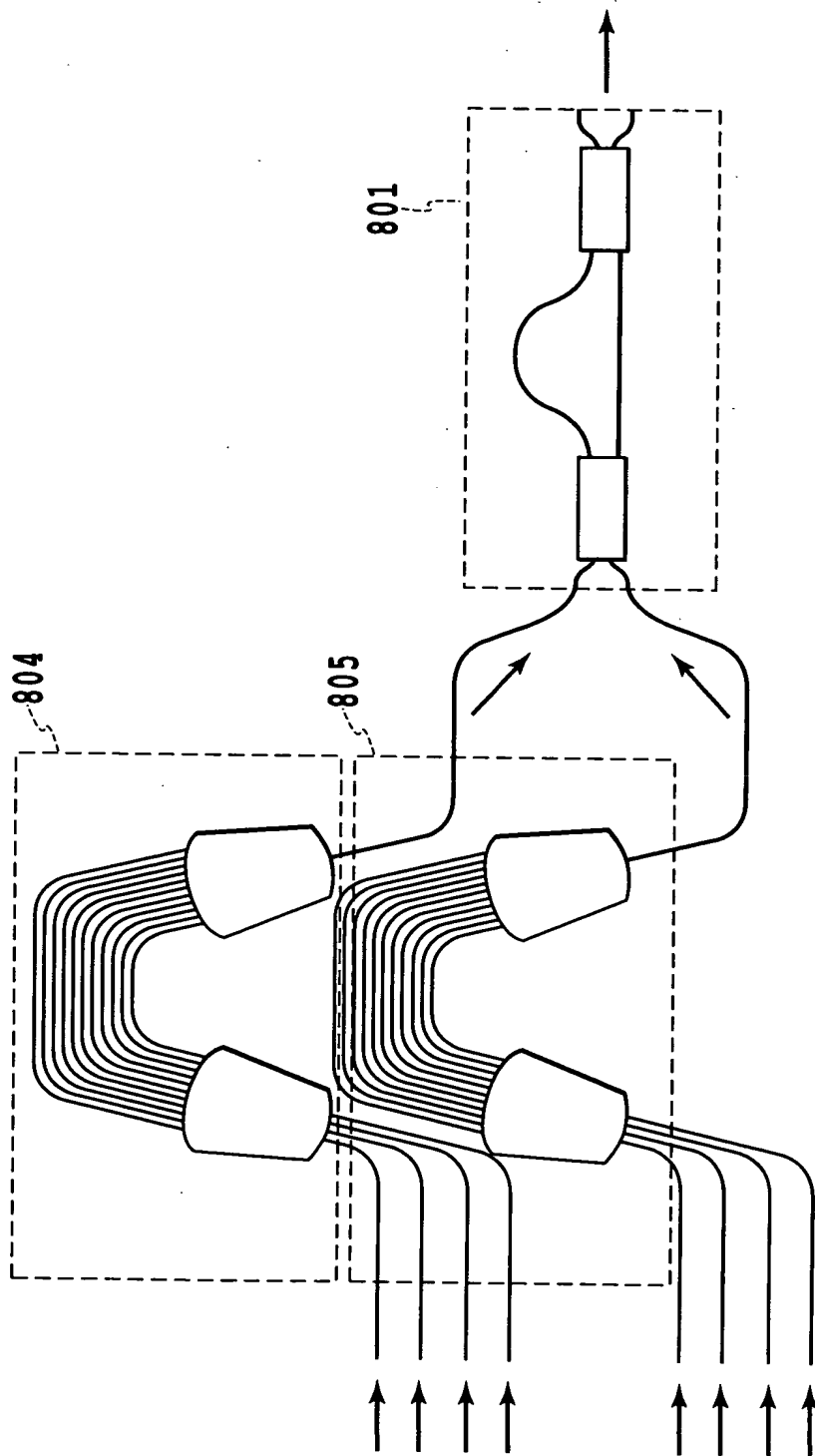


FIG.72

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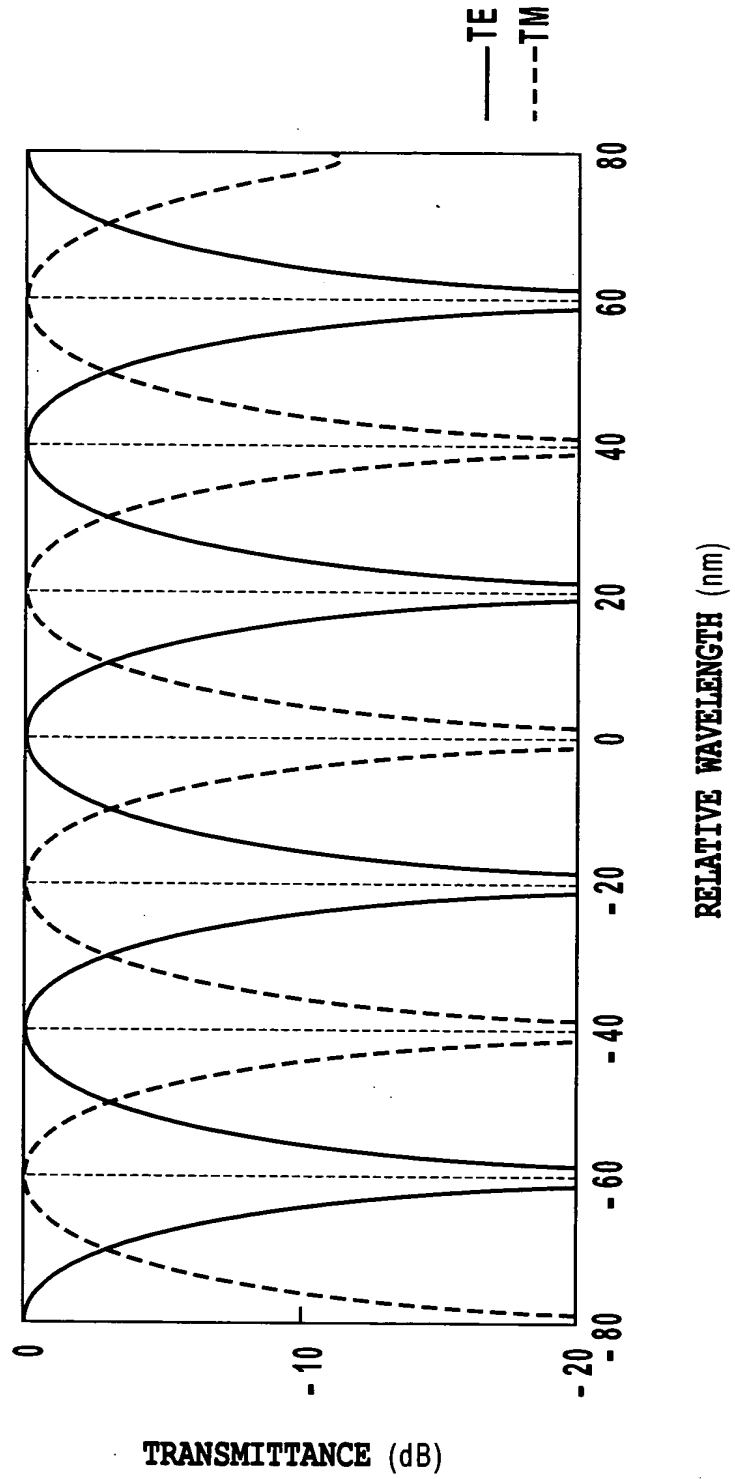


FIG.73

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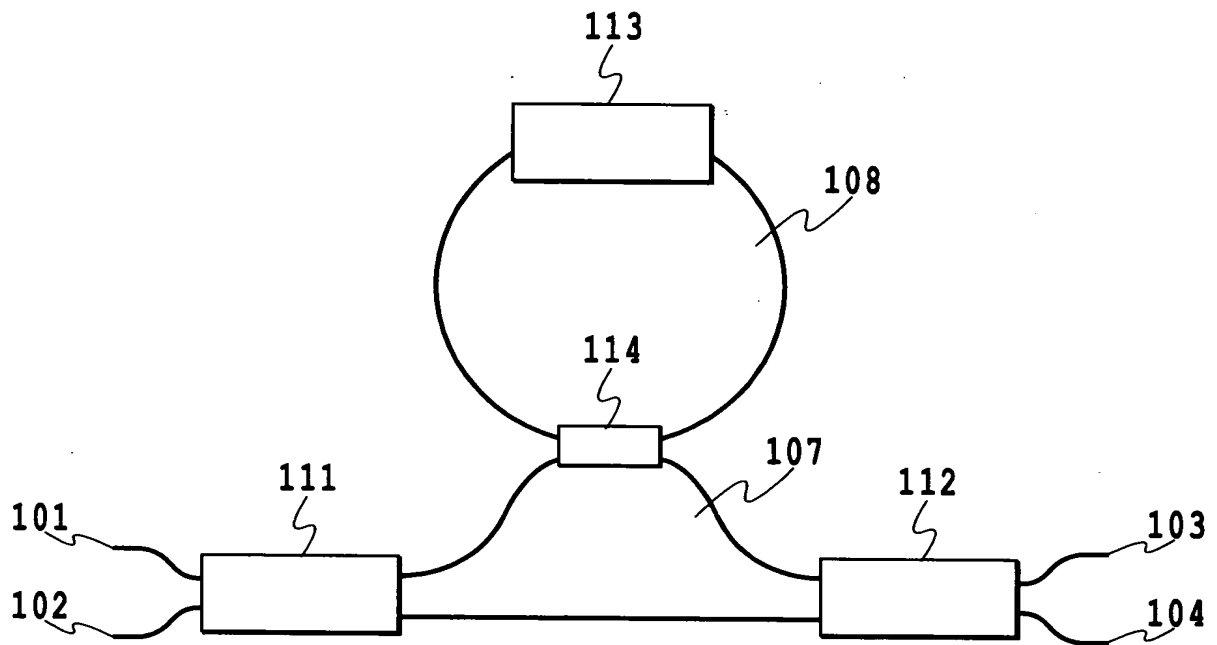


FIG.74

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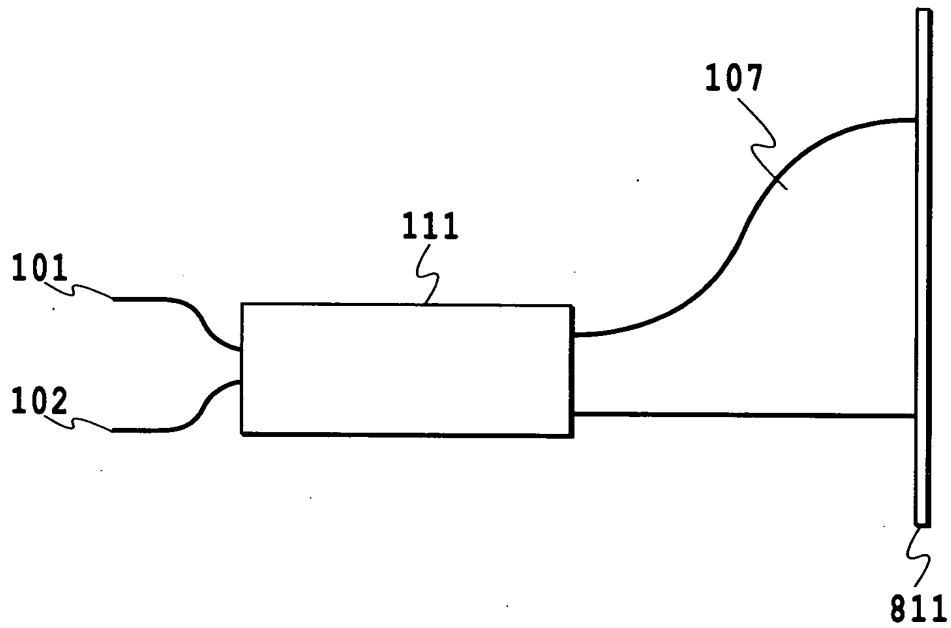


FIG.75

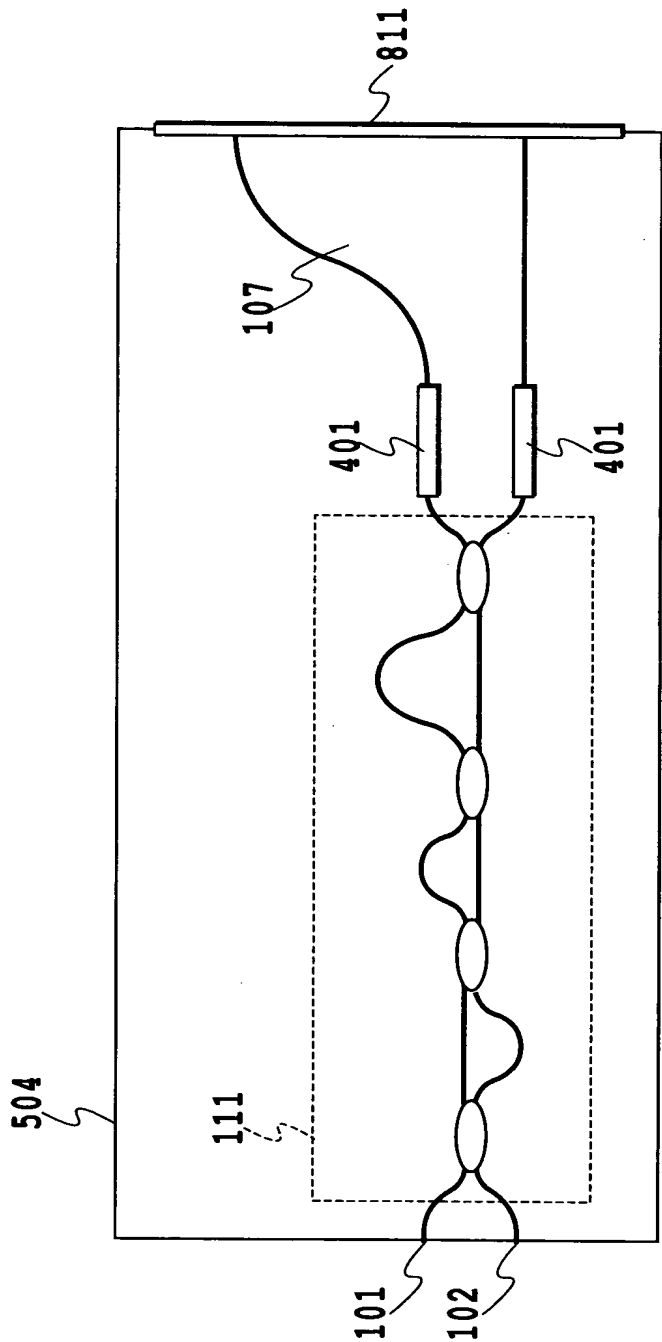


FIG.76

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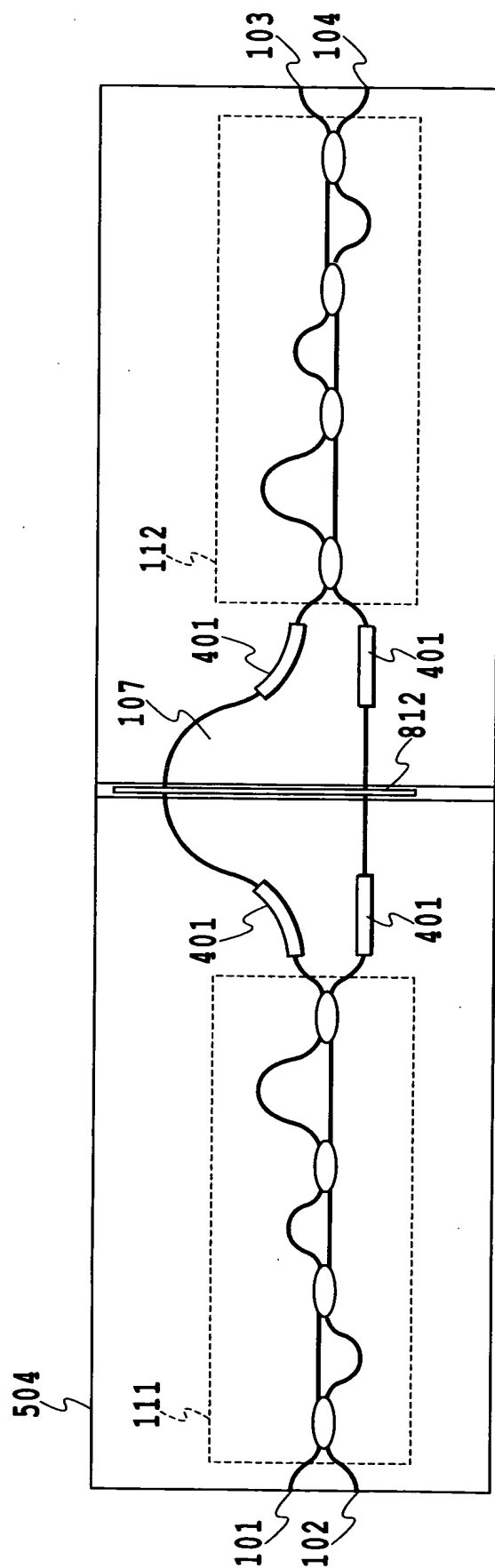


FIG.77

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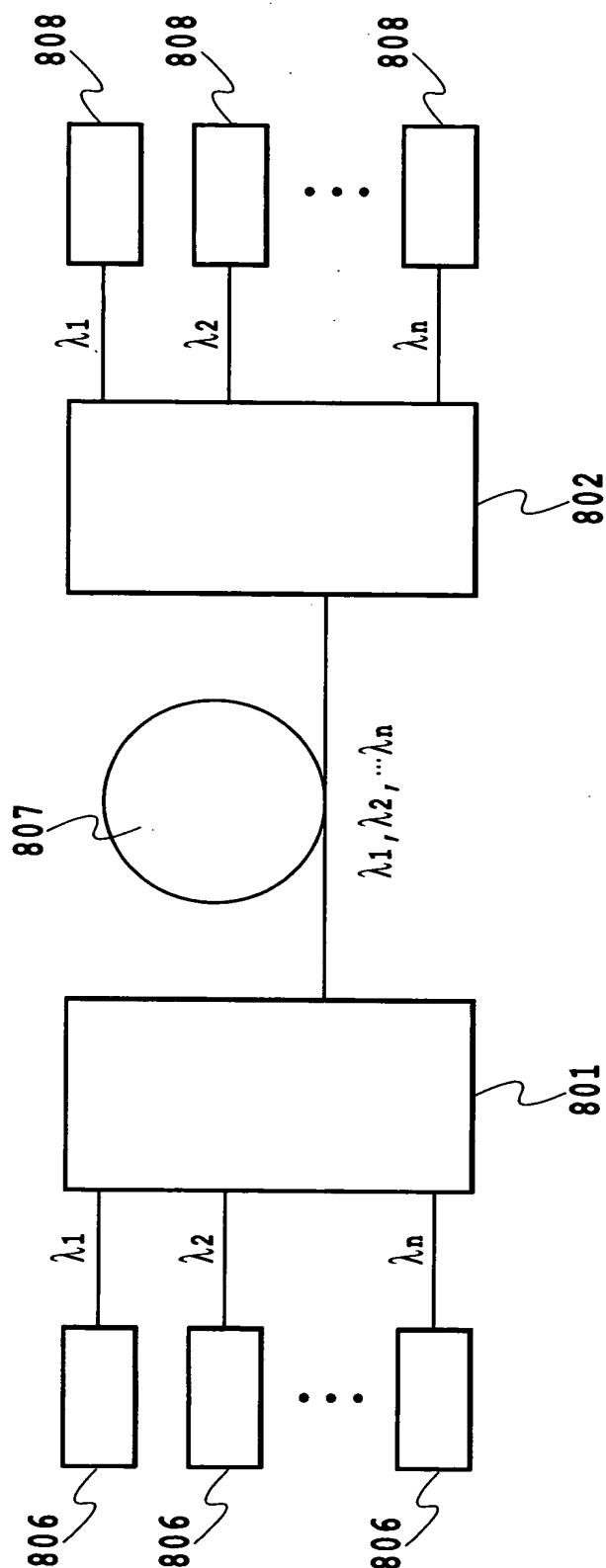


FIG. 78

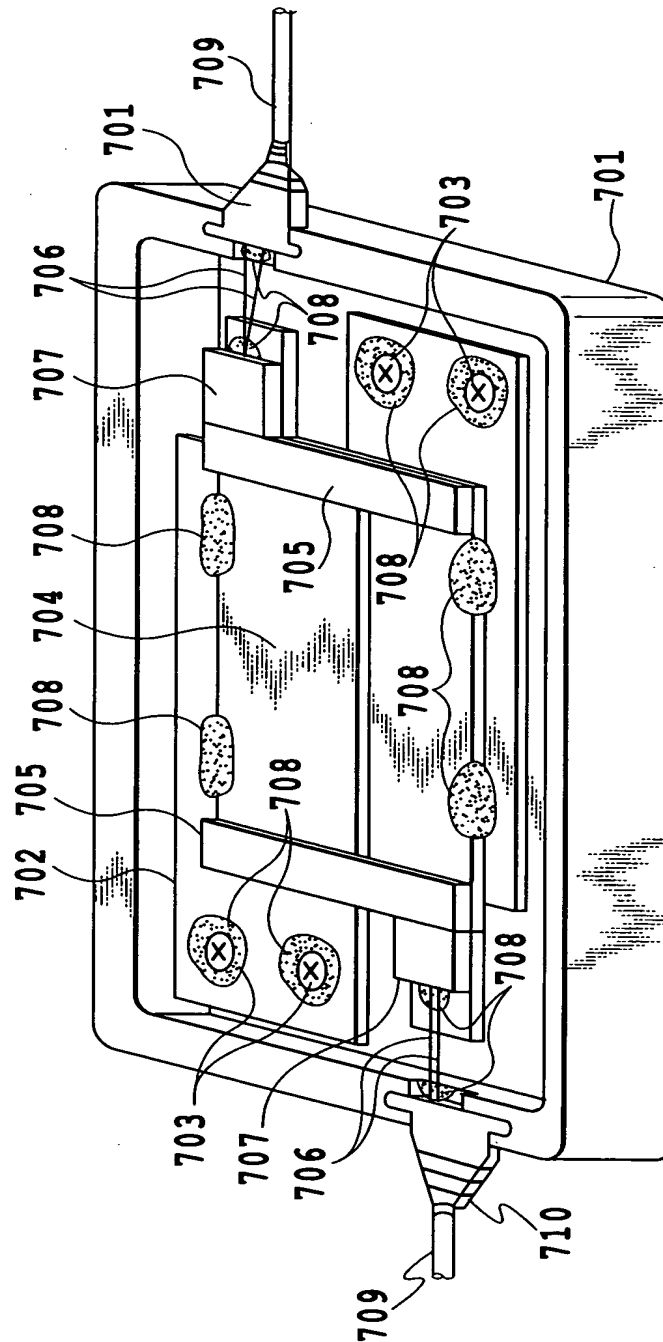


FIG. 79

Title: OPTICAL MULTI/DEMULTIPLEXING CIRCUIT
EQUIPPED WITH PHASE GENERATING DEVICE

Inventors: Takayuki Mizuno et al.

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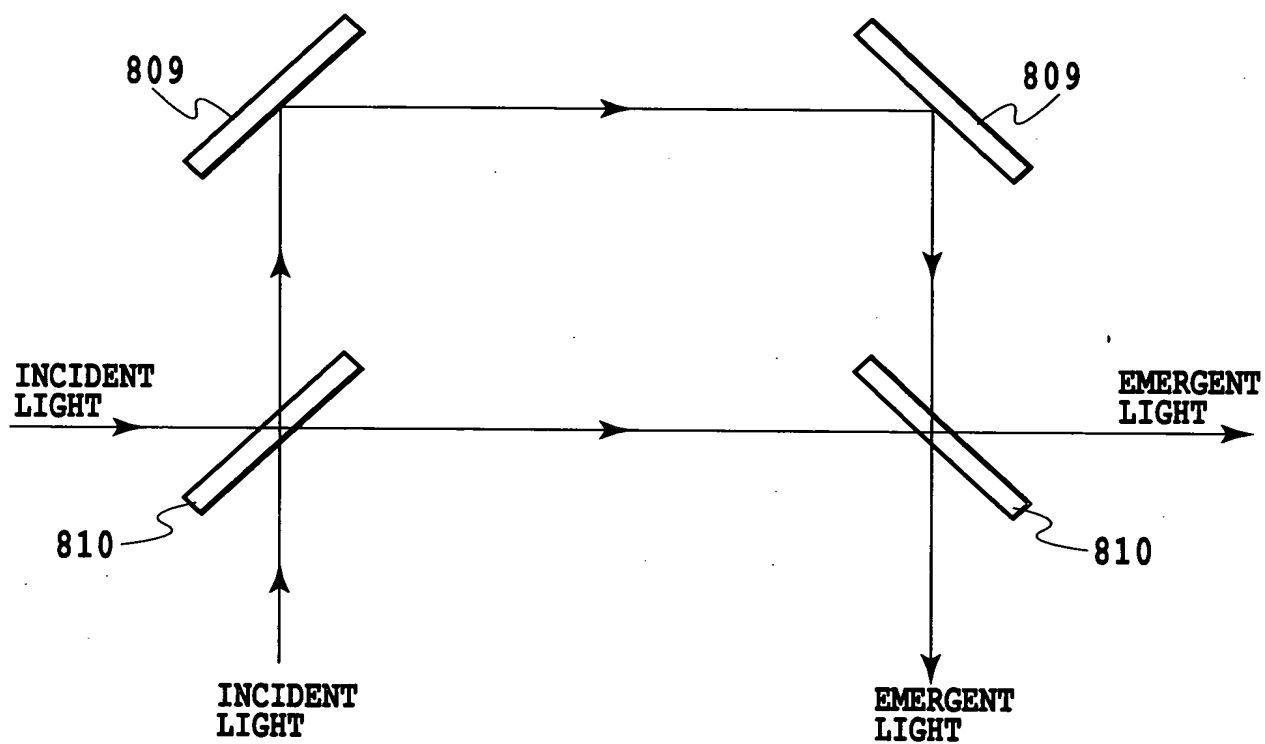


FIG.80